STEREO INTEGRATED AMPLIFIER

A-92

SERVICE MANUAL

KENWO 1680

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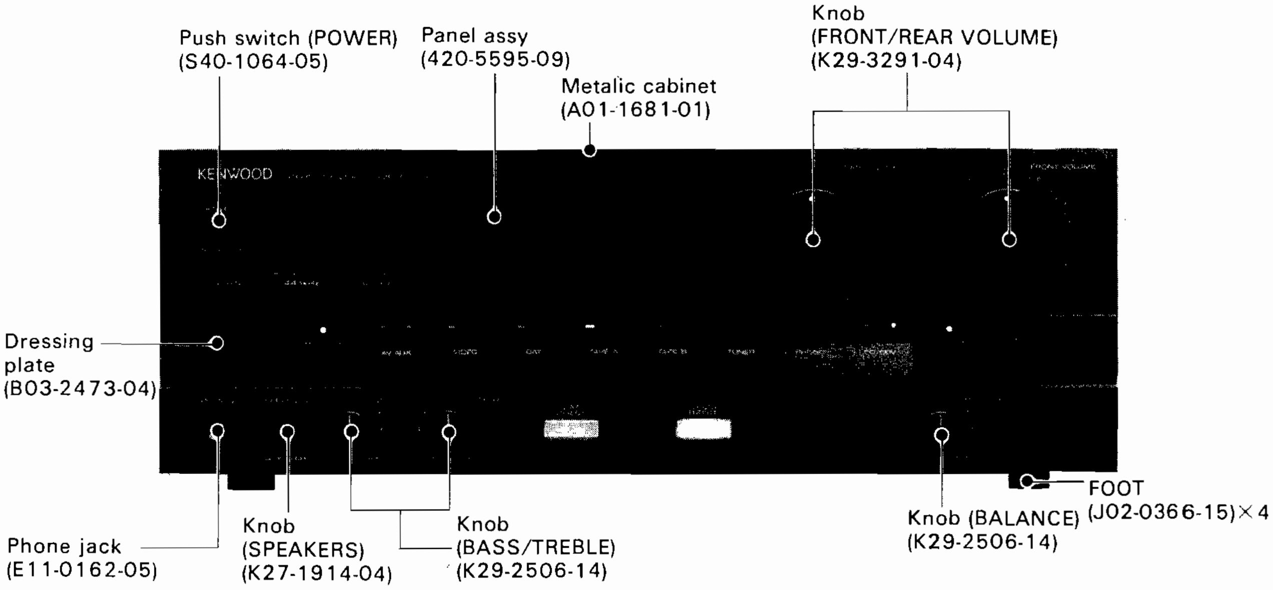
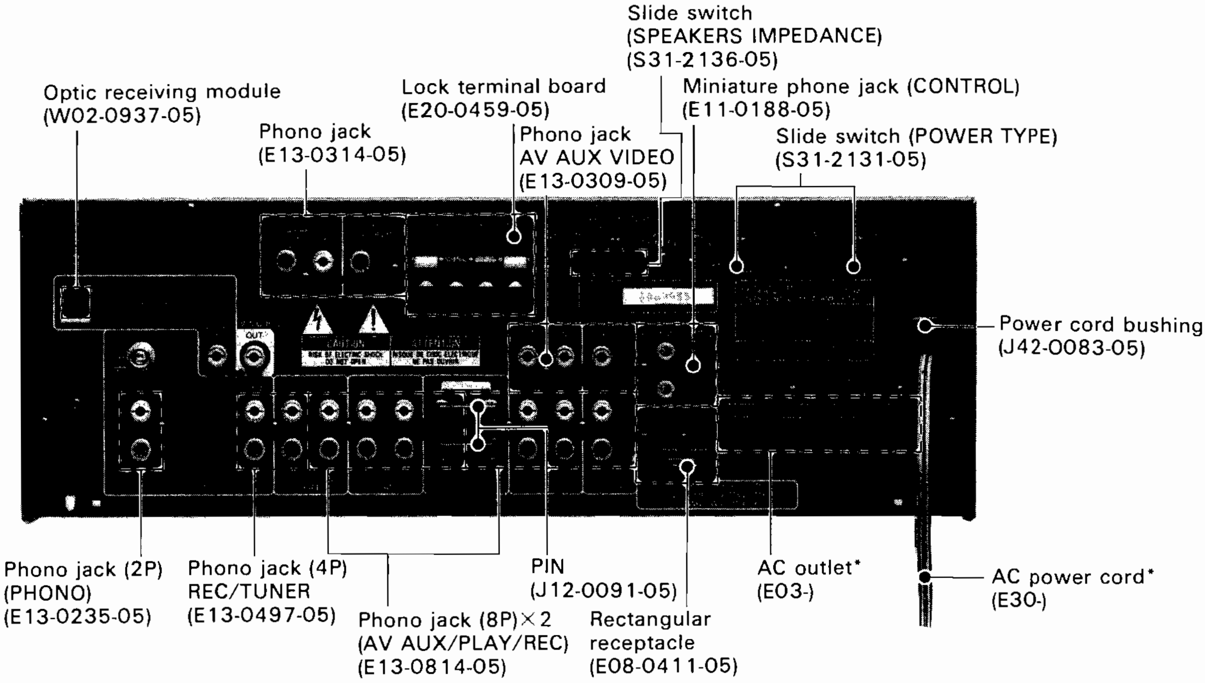
B51-3610-00 (G) 2663


Push switch (POWER)
(S40-1064-05)
Panel assy
(420-5595-09)
Metalic cabinet
(A01-1681-01)
Knob
(FRONT/REAR VOLUME)
(K29-3291-04)
KENWOOD
Dressing plate
(B03-2473-04)
Phone jack
(E11-0162-05)
Knob
(SPEAKERS)
(K27-1914-04)
Knob
(BASS/TREBLE)
(K29-2506-14)
Knob (BALANCE)
(K29-2506-14)
FOOT
(J02-0366-15)×4


Slide switch
(SPEAKERS IMPEDANCE)
(S31-2136-05)
Optic receiving module
(W02-0937-05)
Lock terminal board
(E20-0459-05)
Miniature phone jack (CONTROL)
(E11-0188-05)
Phono jack
(E13-0314-05)
Phono jack
AV AUX VIDEO
(E13-0309-05)
Slide switch (POWER TYPE)
(S31-2131-05)
OUT
Power cord bushing
(J42-0083-05)
Phono jack (2P)
(PHONO)
(E13-0235-05)
Phono jack (4P)
REC/TUNER
(E13-0497-05)
Phono jack (8P)×2
(AV AUX/PLAY/REC)
(E13-0814-05)
PIN
(J12-0091-05)
Rectangular
receptacle
(E08-0411-05)
AC outlet*
(E03-)
AC power cord*
(E30-)

* Refer to parts list on page 47.

1680

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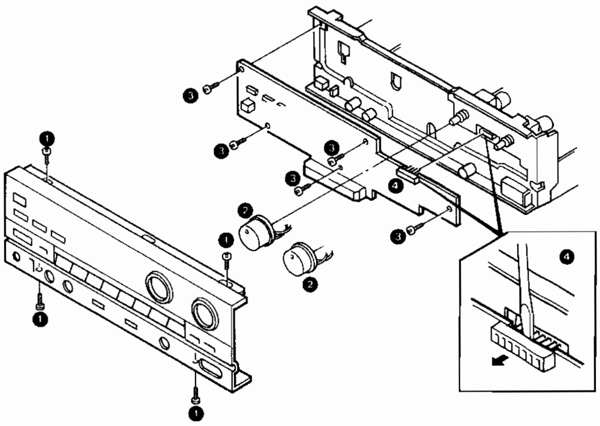
CAUTION

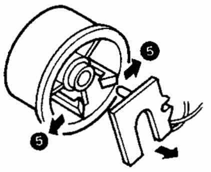
- The AC power supply for the X-91 on the rear panel outputs 16 V AC. Therefore, it cannot be used for supplying AC power to the X Series.
- In addition to conventional audio circuitry, this unit is equipped with 4 video input systems and 2 video output systems.

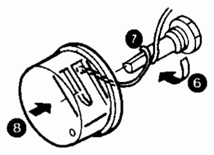
DISASSEMBLY FOR REPAIR

1. Remove the 4 screws (❶) fixing the front panel.
2. Remove the 2 Volume control knobs (❷). As LED cords are connected to them, take care not to pull them too violently.
3. Remove the 5 screws (❸) fixing the Switch Unit and disengage its connector (❹) as shown in the illustration.

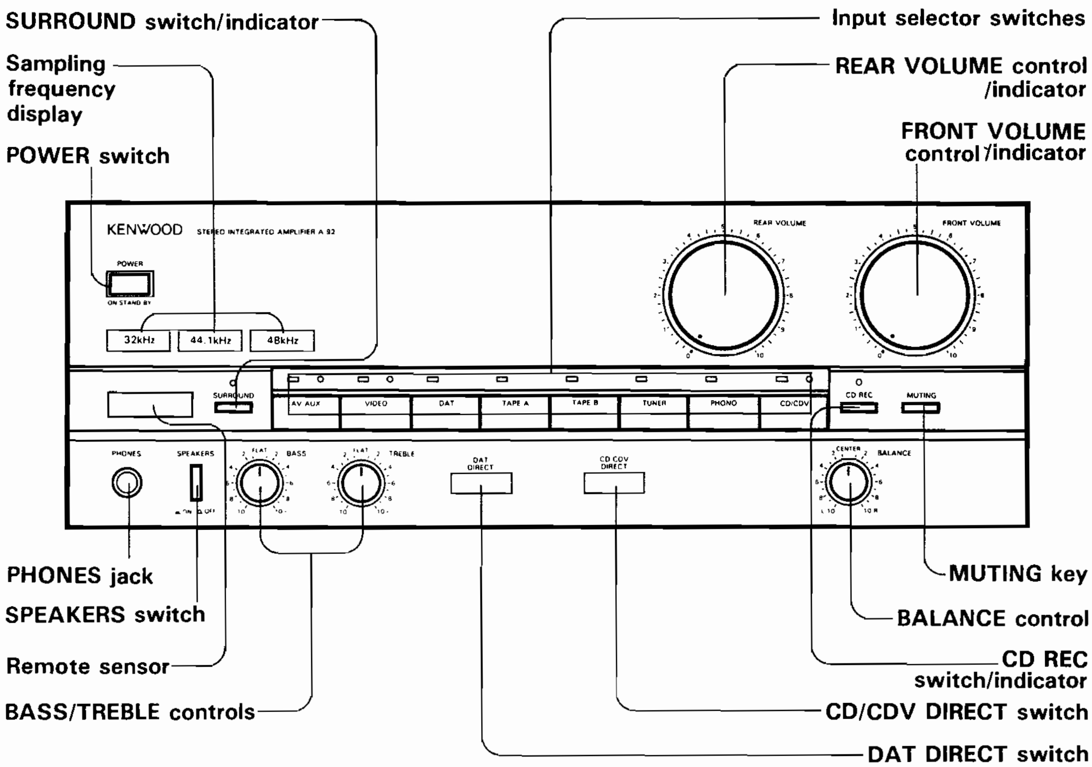


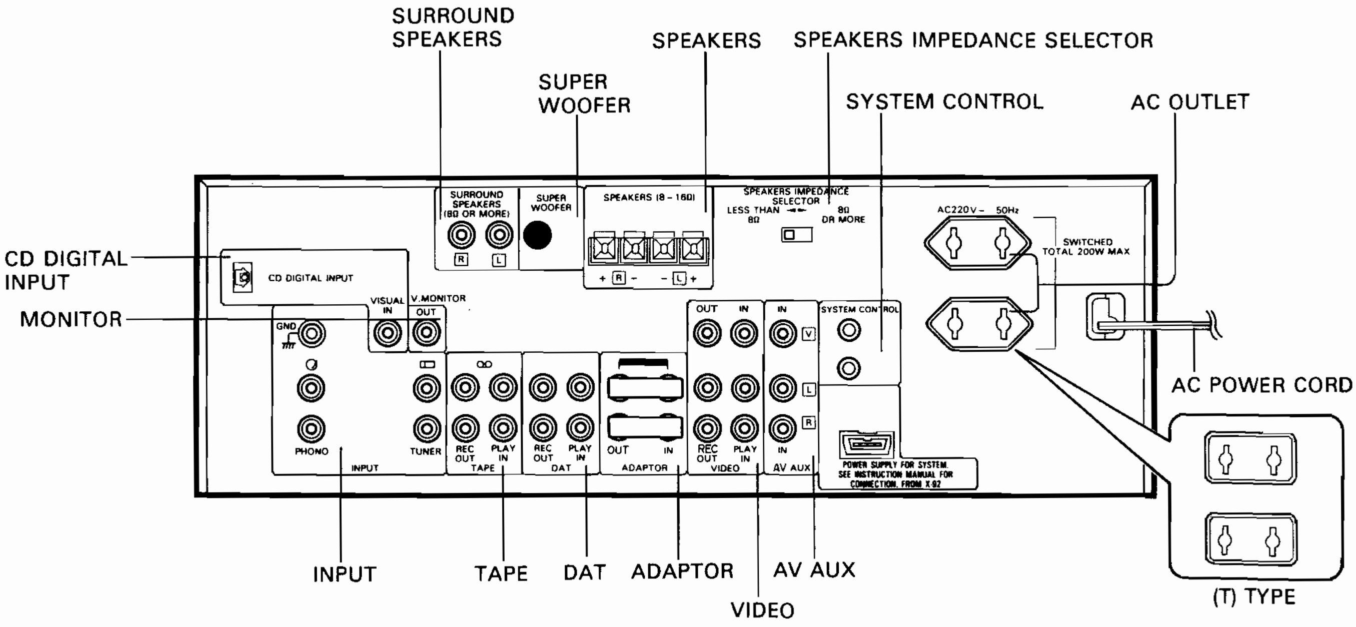
4. Disengage the 2 claws (❺) retaining the Volume control LED Unit, and take out the PC board.
5. When attaching the Volume control knobs to the VRs, set each VR to the maximum position (❻), wind the cord once around the shaft (❼), then insert the control knob (❽).



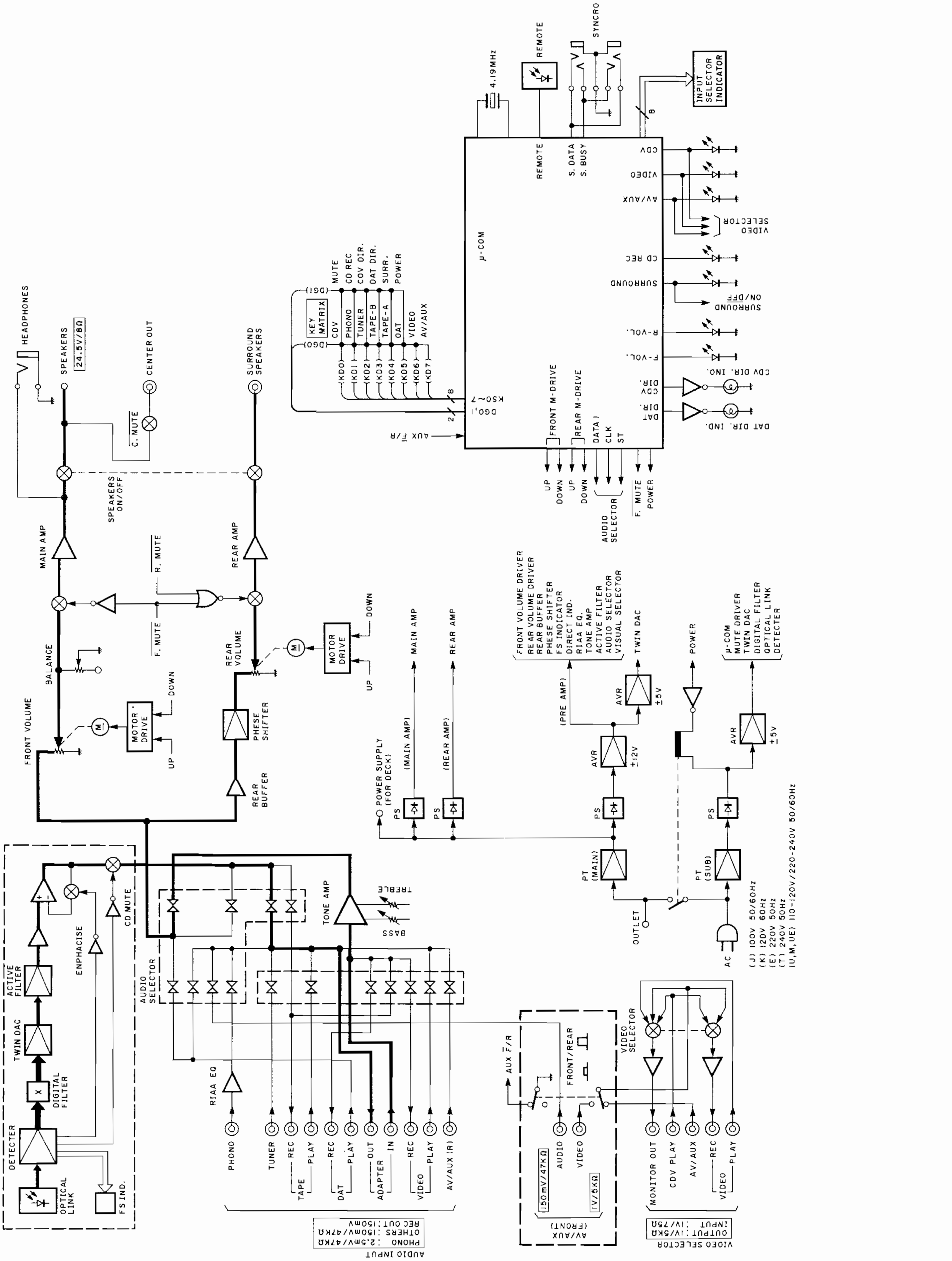


FUNCTION OF AMPLIFIER


SURROUND switch/indicator
Sampling frequency display
POWER switch
Input selector switches
REAR VOLUME control /indicator
FRONT VOLUME control /indicator
KENWOOD
STEREO INTEGRATED AMPLIFIER A 92
POWER
ON STAND BY
32kHz
44.1kHz
48kHz
REAR VOLUME
FRONT VOLUME
SURROUND
AV AUX
VIDEO
DAT
TAPE A
TAPE B
TUNER
PHONO
CD/CDV
CD REC
MUTING
PHONES
SPEAKERS
FLAT
BASS
TREBLE
DAT DIRECT
CD CDV DIRECT
CENTER
BALANCE
PHONES jack
SPEAKERS switch
Remote sensor
BASS/TREBLE controls
MUTING key
BALANCE control
CD REC switch/indicator
CD/CDV DIRECT switch
DAT DIRECT switch


SURROUND SPEAKERS
SUPER WOOFER
SPEAKERS
SPEAKERS IMPEDANCE SELECTOR
SYSTEM CONTROL
AC OUTLET
CD DIGITAL INPUT
MONITOR
SURROUND SPEAKERS (8Ω OR MORE)
R
L
SUPER WOOFER
SPEAKERS (8 – 16Ω)
+ R –
– L +
SPEAKERS IMPEDANCE SELECTOR
LESS THAN 8Ω
8Ω OR MORE
AC220V ~ 50Hz
SWITCHED TOTAL 200W MAX
CD DIGITAL INPUT
VISUAL IN
V.MONITOR OUT
GND
PHONO
TUNER
INPUT
REC OUT
PLAY IN
TAPE
REC OUT
PLAY IN
DAT
OUT
IN
ADAPTOR
REC OUT
PLAY IN
VIDEO
IN
V
L
R
AV AUX
SYSTEM CONTROL
POWER SUPPLY FOR SYSTEM. SEE INSTRUCTION MANUAL FOR CONNECTION. FROM X-92
AC POWER CORD
INPUT
TAPE
DAT
ADAPTOR
AV AUX
VIDEO
(T) TYPE

BLOCK LEVEL DIAGRAM


HEADPHONES
SPEAKERS
24.5V/8Ω
CENTER OUT
C. MUTE
SURROUND SPEAKERS
SPEAKERS ON/OFF
MAIN AMP
REAR AMP
R. MUTE
F. MUTE
BALANCE
FRONT VOLUME
REAR VOLUME
MOTOR DRIVE
UP
DOWN
M
PHESE SHIFTER
REAR BUFFER
OPTICAL LINK
DETECTER
DIGITAL FILTER
TWIN DAC
ACTIVE FILTER
ENPHACISE
CD MUTE
FS IND.
AUDIO SELECTOR
TONE AMP
TREBLE
BASS
RIAA EQ
PHONO
TUNER
REC
PLAY
TAPE
DAT
OUT
IN
ADAPTER
VIDEO
AV/AUX (R)
AUDIO INPUT
PHONO : 2.5mV/47kΩ
OTHERS : 150mV/47kΩ
REC OUT : 150mV
AUX F/R
FRONT/REAR
AUDIO
VIDEO
150mV/47kΩ
1V/5kΩ
AV/AUX (FRONT)
VIDEO SELECTOR
MONITOR OUT
CDV PLAY
AV/AUX
VIDEO SELECTOR
INPUT : 1V/75Ω
OUTPUT : 1V/5kΩ
POWER SUPPLY (FOR DECK)
PS
(MAIN AMP)
(REAR AMP)
MAIN AMP
REAR AMP
PT (MAIN)
PT (SUB)
OUTLET
AC
(J) 100V 50/60Hz
(K) 120V 60Hz
(E) 220V 50Hz
(T) 240V 50Hz
(U,M,UE) 110-120V/220-240V 50/60Hz
AVR
±12V
±5V
+5V
(PRE AMP)
FRONT VOLUME DRIVER
REAR VOLUME DRIVER
REAR BUFFER
PHESE SHIFTER
FS INDICATOR
DIRECT IND.
RIAA EQ.
TONE AMP
ACTIVE FILTER
AUDIO SELECTOR
VISUAL SELECTOR
TWIN DAC
POWER
μ-COM
MUTE DRIVER
TWIN DAC
DIGITAL FILTER
OPTICAL LINK
DETECTER
4.19MHz
REMOTE
SYNCRO
INPUT SELECTOR INDICATOR
8
S. DATA
S. BUSY
μ-COM
CDV
VIDEO
AV/AUX
VIDEO SELECTOR
CD REC
SURROUND
SURROUND ON/OFF
R-VOL.
F-VOL.
CDV DIR.
CDV DIR. IND.
DAT DIR.
DAT DIR. IND.
FRONT M-DRIVE
REAR M-DRIVE
DATA I
CLK
ST
UP
DOWN
AUDIO SELECTOR
F. MUTE
POWER
AUX F/R
DG0,1
KS0~7
2
KD0
KD1
KD2
KD3
KD4
KD5
KD6
KD7
DG0
DG1
KEY MATRIX
CDV
PHONO
TUNER
TAPE-B
TAPE-A
DAT
VIDEO
AV/AUX
MUTE
CD REC
CDV DIR.
DAT DIR.
SURR.
POWER

CIRCUIT DESCRIPTION

Description of Components

AUDIO UNIT (X09-2500-11)

Component	Use/Function	Operation/Condition/Compatibility
IC1 (TC9163N) IC2 (TC9164N)	Input Signal Select	Selects each input signal by command from the microcomputer.
IC4 (μPC4570C)	Surround Buffer	Buffer amp for rear speakers.
IC5 (μPC4570C)	Surround Buffer	Surround matrix amp for rear speakers.
IC6 (μPC7812HF)	AVR	AVR for +12 V
Q1 ~ 4	Power Transistor	For driving speakers.
Q5, 6	Temperature Compensator	For controlling idling current.
Q7	AVR	Error signal amplifying.
Q8	AVR	Voltage control.
Q9	Surround Control	Controlling the center channel.
Q10	Surround Output Mute	Muting control of center channel.
Q11	Power Control	Power supply control when the power button is turned ON/OFF.

Preamplifier (X85-1030-11): DT1

Component	Use/Function	Operation/Condition/Compatibility
IC1 (AN6556)	Phono EQ Amp	

Main amplifier (X89-1010-07): DT2

Component	Use/Function	Operation/Condition/Compatibility
Q1 ~ 4	Input Differential Amp	Q1, 3 (L) and Q2, 4 (R) make up the differential amplifier.
Q5 ~ 8	Secondary Differential Amp	Q5, 7 (L) and Q6, 8 (R) make up the differential amplifier.
Q9, 10	Current Mirror Amp	Consisting of Q9 (L) and Q10 (R).
Q11 ~ 14	Drive Amp	Consisting of Q11, 13 (L) and Q12, 13 (R).
Q18	Power Supply Control	For controlling the power at the rising and falling edges of power supply.
Q19	Power Supply Control	For controlling the primary positive (+) power.

Bafa unit (X13-5800-00): DT3

Component	Use/Function	Operation/Condition/Compatibility
IC1 (μPC4570C)	Buffer Amp	

Sub unit (X13-6140-00): DT4

X13-1640 is supplied only for use with μ-com (CXP5016-313S)

Component	Use/Function	Operation/Condition/Compatibility
IC1 (LC4966)	Switch	DAT REC OUT control.
Q1 ~ 3	IC1 Control	Controls the analog switch by command from the microcomputer.

CAUTION: The PC board of the X13-6140-00 unit is deleted following the change of microprocessor (CXP5016-313S → CXP5016-315S).

CIRCUIT DESCRIPTION

CONTROL UNIT (X11-2560-00)

Component	Use/Function	Operation/Condition/Compatibility
IC1 (CXP5016-313S)	Microcomputer	Receives the signal from the remote control unit. ON/OFF control of the main transformer. Controls mutual communication with serial data. AV input/output selector control and display. Controls front/rear electronic (motor-driven) volume control and turning ON/OFF of muting function.
IC2 (M51951 A8L)	Microcomputer Reset	Operates when the AC power plug is inserted into the AC outlet to maintain the initial condition. Also operates when the power switch is turned OFF to store the status immediately before turning the power OFF.
IC3, 4 (LB1641)	Front/Rear Electronic (Motor-driven) Volume	Controls the motor revolution direction (clockwise or counterclockwise) with each high level signal transmitted from the microcomputer to adjust the front speaker and rear speaker volume level.
IC5,6 (μPC1238V•1)	Power Amp	Monolithic power amp with loading short-circuit protection circuit.
IC7 (μPC4570C)	Op Amp	Tone control amp for L/R-channels.
Q1 ~ 4	LED Driver	LED lighting driver for FS display.
Q5,6	Lamp Driver	Direct indicator lamp lighting driver.
Q7,8,10,11	Muting	Muting for front and rear channel output levels.
Q9,12	Muting Control	Output muting control.
A1	Remote Control	Receives the infrared signal exclusively from the remote control unit, and transmits it to the microcomputer.

POWER SUPPLY UNIT (X00-2520-00)

Component	Use/Function	Operation/Condition/Compatibility
IC1 (μPC7805μF)	AVR	+5 V supply (digital circuitry)
Q1	Vide output buffer	For Monitor output
Q2	Vide output buffer	For VIDEO REC output
Q3	Power relay SW	ON when POWER signal is High.
Q4	Tr SW	For CDV Monitor output
Q5	Tr SW	For CDV REC output
Q6	Tr SW	For VTR input and Monitor
Q7	Tr SW	For BS/AUX input and Monitor
Q8	Tr SW	For BS/AUX input and REC output

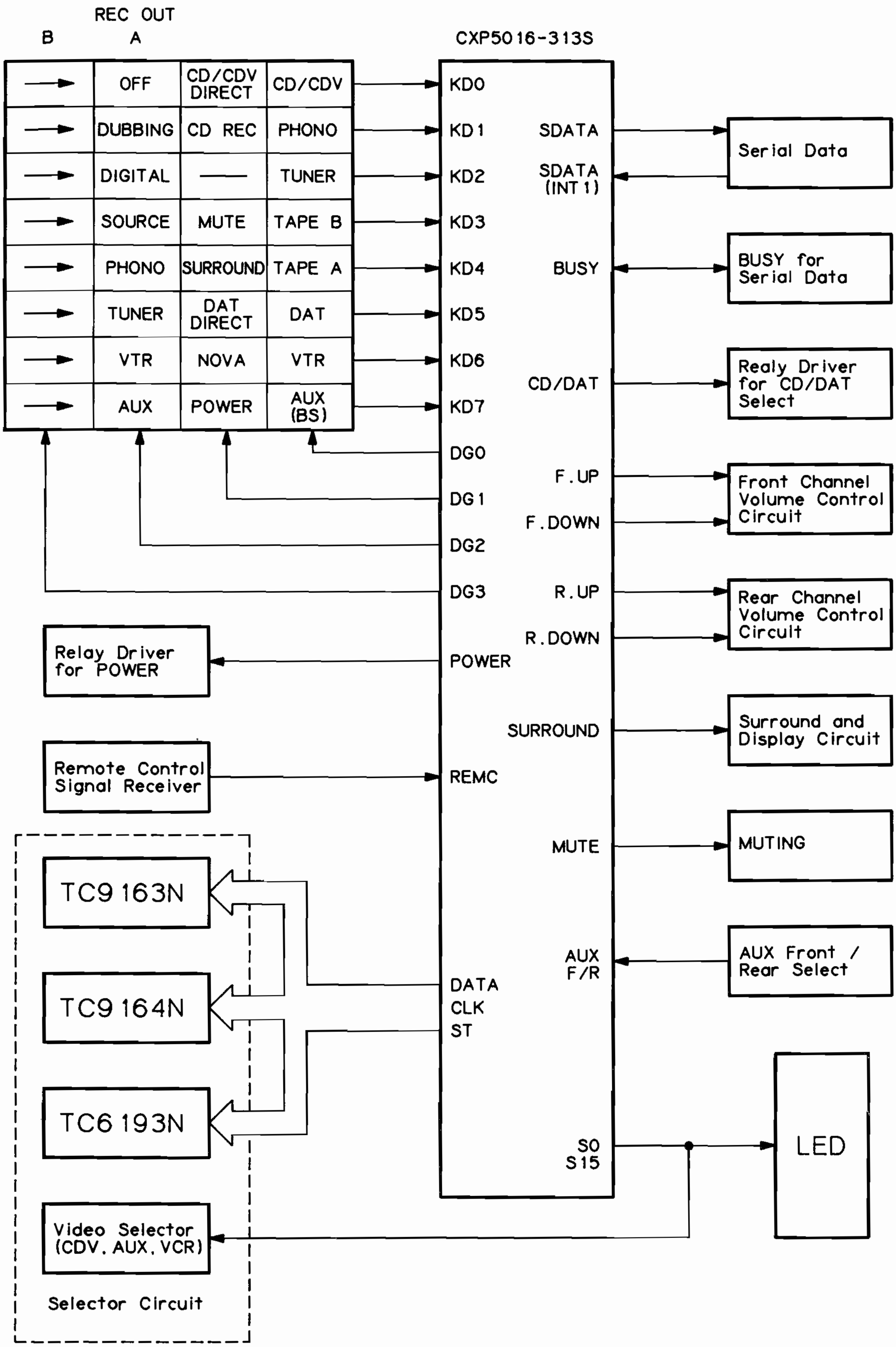
CIRCUIT DESCRIPTION

SIGNAL PROCESSOR UNIT (X32-1330-00)

Component	Use/Function	Operation/Condition/Compatibility
A1 (TORX174)	Optical reception module	Converts optical data transmitted via the optical transfer link into electrical signals.
IC1 (TC74HCOOP)	Waveform shaper	NAND gate × 4
IC2 (YM3623B)	Digital audio format signal converter	Refer to the information on IC2.
IC3 (SM5807EP)	Digital filter	Quadruple oversampling. Refer to the information on IC3.
IC4.5 (PCM56P-L)	D/A converter	
IC6 (UPC4574C)	Active filter opamp	5-stage low-pass filter. (Refer to the information.)
IC7 (μPC4570C)	Output buffer	
Q1	Transistor SW	X'tal oscillator OFF. The X'tal oscillates in case error signal is generated.
Q2	Transistor SW	Audio output muting. The muting is switched ON in case error signal is generated.
Q3	L/R CLK inverter	Inverter transistor for separating DATA (L/R CLK) from digital filter into L and R signals supplied to D/A converter. The inverted signal is passed along to L CH.
Q4, 5	Transistor SW	For emphasis. ON when DEF terminal of IC2 is H.
Q6, 7	Transistor SW	For muting.
Q8	Transistor SW	Converts the logic level of muting control signal.
Q9	AVR	–5 V power supply.
Q10	AVR	+5V power supply
Q11, 12	Transistor SW	Converts the logic level of emphasis control signal

CIRCUIT DESCRIPTION

1-1. Block Diagram of the Microprocessor and Its Surroundings

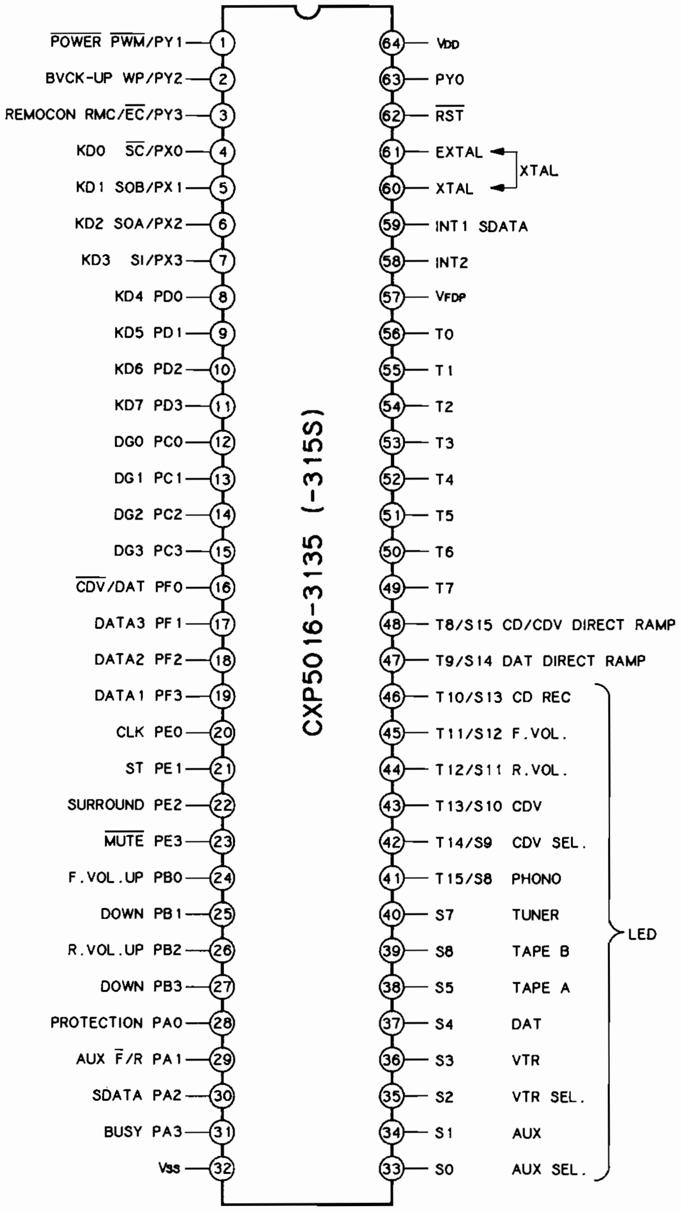


REC OUT B	REC OUT A		
→	OFF	CD/CDV DIRECT	CD/CDV
→	DUBBING	CD REC	PHONO
→	DIGITAL	—	TUNER
→	SOURCE	MUTE	TAPE B
→	PHONO	SURROUND	TAPE A
→	TUNER	DAT DIRECT	DAT
→	VTR	NOVA	VTR
→	AUX	POWER	AUX (BS)

CXP5016-313S
KD0
KD1
KD2
KD3
KD4
KD5
KD6
KD7
DG0
DG1
DG2
DG3
POWER
REMC
DATA
CLK
ST
SDATA
SDATA (INT1)
BUSY
CD/DAT
F.UP
F.DOWN
R.UP
R.DOWN
SURROUND
MUTE
AUX F/R
S0
S15
Serial Data
BUSY for Serial Data
Realy Driver for CD/DAT Select
Front Channel Volume Control Circuit
Rear Channel Volume Control Circuit
Surround and Display Circuit
MUTING
AUX Front / Rear Select
LED
Relay Driver for POWER
Remote Control Signal Receiver
TC9163N
TC9164N
TC6193N
Video Selector (CDV, AUX, VCR)
Selector Circuit

CIRCUIT DESCRIPTION

1-2. Pin Assignment


POWER PWM/PY1 1
BVCK-UP WP/PY2 2
REMOCON RMC/EC/PY3 3
KD0 SC/PX0 4
KD1 SOB/PX1 5
KD2 SOA/PX2 6
KD3 SI/PX3 7
KD4 PD0 8
KD5 PD1 9
KD6 PD2 10
KD7 PD3 11
DG0 PC0 12
DG1 PC1 13
DG2 PC2 14
DG3 PC3 15
CDV/DAT PF0 16
DATA3 PF1 17
DATA2 PF2 18
DATA1 PF3 19
CLK PE0 20
ST PE1 21
SURROUND PE2 22
MUTE PE3 23
F.VOL.UP PB0 24
DOWN PB1 25
R.VOL.UP PB2 26
DOWN PB3 27
PROTECTION PA0 28
AUX F/R PA1 29
SDATA PA2 30
BUSY PA3 31
Vss 32
64 VDD
63 PY0
62 RST
61 EXTAL
60 XTAL
XTAL
59 INT1 SDATA
58 INT2
57 VFDP
56 T0
55 T1
54 T2
53 T3
52 T4
51 T5
50 T6
49 T7
48 T8/S15 CD/CDV DIRECT RAMP
47 T9/S14 DAT DIRECT RAMP
46 T10/S13 CD REC
45 T11/S12 F.VOL.
44 T12/S11 R.VOL.
43 T13/S10 CDV
42 T14/S9 CDV SEL.
41 T15/S8 PHONO
40 S7 TUNER
39 S6 TAPE B
38 S5 TAPE A
37 S4 DAT
36 S3 VTR
35 S2 VTR SEL.
34 S1 AUX
33 S0 AUX SEL.
LED
CXP5016-3135 (-315S)

Note : CXP5016-313S will be used in the units manufactured in the period between July and August in 1988, and after then, (SN80900001) it will be replaced with the -315S, which has the same port allocations and compatibility.

CIRCUIT DESCRIPTION

1-3. Explanation of Pins

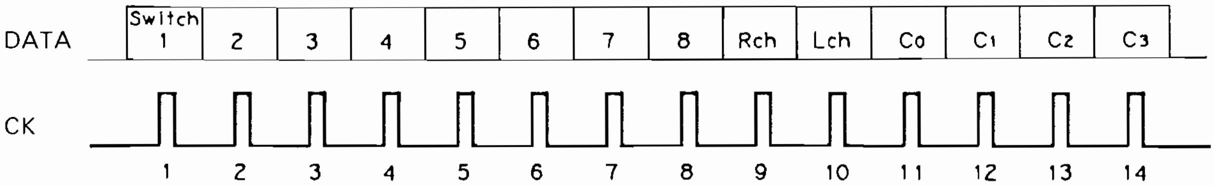
Pin No.	Pin Name	I/O	Name	Function
1	PY1	O	$\overline{\text{POWER}}$	POWER ON/OFF port. Goes low when power is ON and high when power is OFF.
2	PY2	I	BACK-UP	Not used (connected to +5 V)
3	RMC	I	REMOCON	Remote control input pin
4 ~ 7	PX0~PX3	I	KD0~KD3	Key scan input port. Active high.
8 ~ 11	PD0~PD3	I	KD4~KD7	Key scan input port. Active high.
12 ~ 15	PC0~PC3	O	DG0~DG3	Key scan output port.
16	PF0	O	$\overline{\text{CDV}}$/DAT	
17	PF1	O	DATA3	NC
18	PF2	O	DATA2	NC
19	PF3	O	DATA1	For MIDI series: Serial data output port for TC9163N/ TC-9164N.
20	PE0	O	CLK	Serial data clock output port for Selector IC.
21	PE1	O	ST	Serial data S7 output port for Selector IC.
22	PE2	O	SURROUND	Surround ON/OFF and display LED driver port. Goes high when surround is ON, and goes low when it is OFF.
23	PE3	O	$\overline{\text{MUTE}}$	Muting port. Goes low when muting is ON, and high when it is OFF.
24	PB0	O	F.VOL.UP	Front channel volume UP drive port. When it is high, volume level is increased.
25	PB1	O	F.VOL.DOWN	Front channel volume DOWN drive port. When it is high, volume level is decreased.
26	PB2	O	R.VOL. UP	Rear channel volume UP drive port. When it is high, volume level is increased.
27	PB3	O	R.VOL.DOWN	Rear channel volume DOWN drive port. When it is high, volume level is decreased.
28	PA0	I	PROTECTION	
29	PA1	I	AUX F/R	AUX front/rear select input port. Rear AUX is selected when it is high, and front AUX is selected when it is low.
30	PA2	O	SDATA	Serial data output port between system components.
31	PA3	I/O	BUSY	Serial data BUSY I/O port between system components.
32	Vss	O		Ground for microcomputer.
33	S0	O	AUX SEL.	Video selector (AUX) and display LED drive port. Turns ON and LED lights when it is high.
34	S1	O	BC/AUX	Selector AUX (EQ) display LED drive port. LED lights when it is high.
35	S2	O	VTR SEL.	Video selector (AUX) and display LED drive port. Turns ON and LED lights when it is high.
36	S3	O	VTR	Selector VCR display LED drive port. LED lights when it is high.
37	S4	O	DAT	DAT
38	S5	O	TAPE A	TAPE A (1)
39	S6	O	TAPE B	TAPE B (2)
40	S7	O	TUNER	TUNER
41	S8	O	PHONO	PHONO
42	S9	O	CDV SEL.	Video selector (CDV) and display LED drive port. Turns ON and LED lights when it is high.
43	S10	O	CDV	Selector CDV display LED drive port. LED lights when it is high.
44	S11	O	R. VOL	Rear channel volume
45	S12	O	F. VOL	Front channel volume
46	S13	O	CD REC	CD REC
47	S14	O	DAT DIRECT RAMP	DAT Direct indicator lamp
48	S15	O	CDV DIRECT RAMP	CDV Direct indicator lamp
49~56	T7~T0			Not used (OPEN)
57	VFDP	I		Loading power supply pin
58	INT2			Not used (GND)
59	INT1	I	SDATA	Serial data input port between system components.
60	XTAL	O		Clock generator output port.
61	EXTAL	I		Clock generator input port.
62	RST			Reset signal input, and built-in power ON reset circuit output pin.
63	PYO			Not used (Pull-up)
64	VDD			Positive power supply pin of the microcomputer.

CIRCUIT DESCRIPTION

2. Selector IC Operation

Data input

TC9162/63/64N can control each analog switch arbitrarily by inputting the specified data to the DATA, CK and ST pins. The data consists of 14 bits which are allocated as follows:



Since the 1st ~ 8th bits are corresponding to the analog switches 1 ~ 8, set the bit level to '1' corresponding to the switch to be turned ON.

The 9th and 10th bits are the left and right channel select bits.
When this bit level is set to '1', the corresponding channel is selected. Therefore, both L/R simultaneous status ('1', '1') or L/R independent status ('1', '0' or '0', '1') can also be set.

The 11th bit to 14th bit are the code bits to be used for chip select.
For example, when TC9162N, TC9163N and TC9164N are used simultaneously, DATA, CK and ST pins are connected commonly and any of TC9162N, TC9163N and TC9164N can be selected by these code bit data.

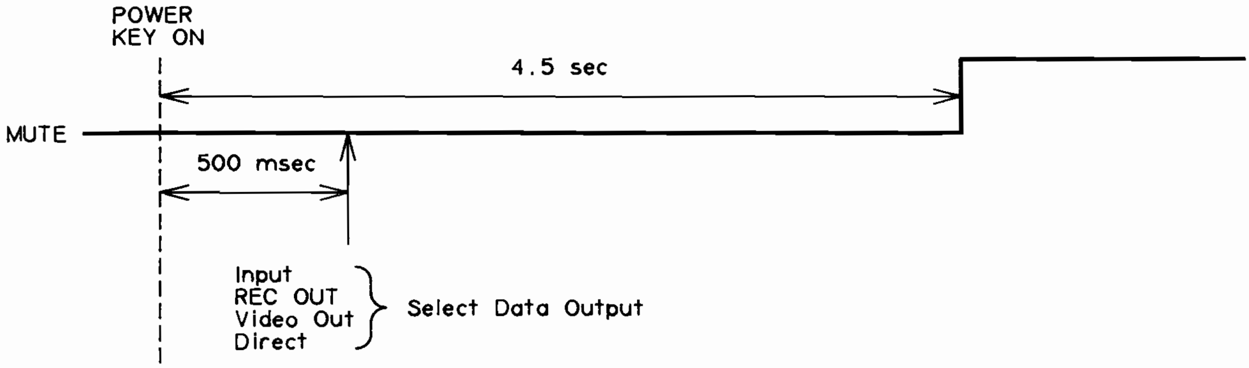
Chip select codes are set as follows:

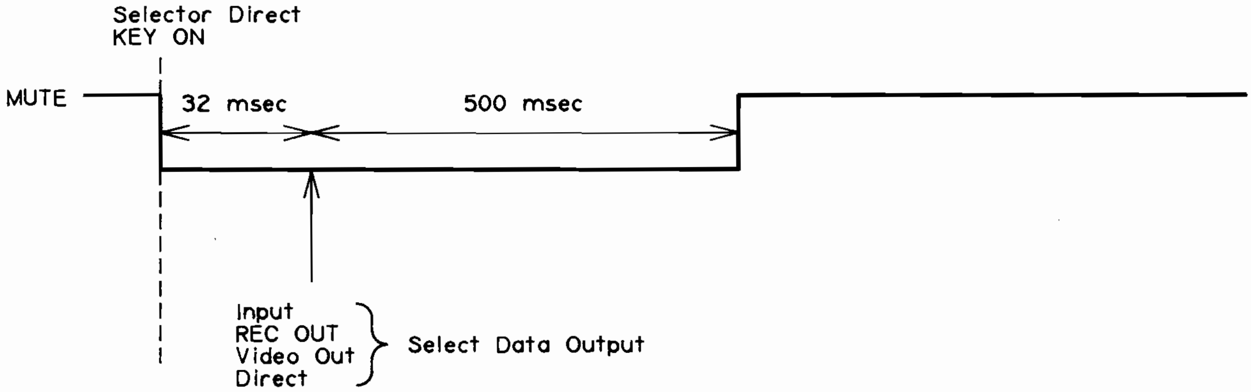
	C_0	C_1	C_2	C_3
TC9162N	0	0	0	0
TC9163N	1	0	0	0
TC9164N	0	1	0	0

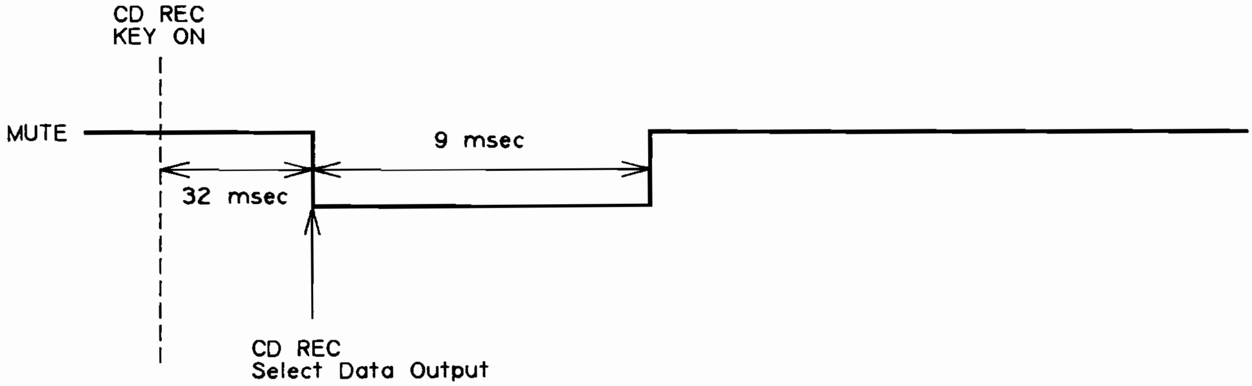
CIRCUIT DESCRIPTION

3. Timing Chart

Muting timing

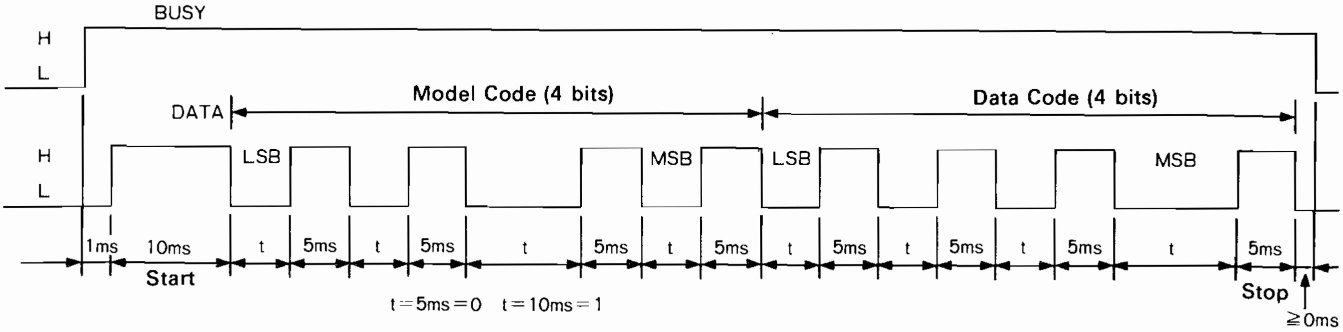

POWER
KEY ON
4.5 sec
MUTE
500 msec
Input
REC OUT
Video Out
Direct
Select Data Output


Selector Direct
KEY ON
MUTE
32 msec
500 msec
Input
REC OUT
Video Out
Direct
Select Data Output


CD REC
KEY ON
MUTE
9 msec
32 msec
CD REC
Select Data Output

CIRCUIT DESCRIPTION

4-1. Explanation of Serial Data Communication

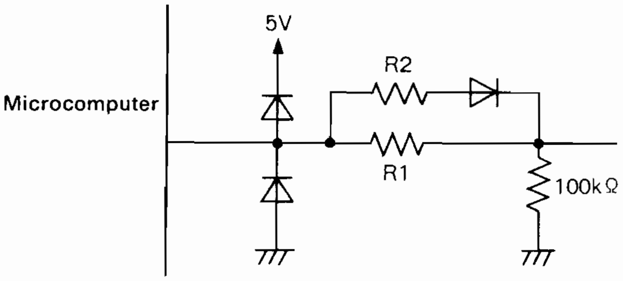


The serial data communication format is defined as shown above, in which the BUSY/DATA 2-wire full-duplex bus is used. 1 word consists of 8 bits, and the most significant 4 bits are defined as the model code of the transmitter (4 = CD player in the above example) while the subsequent 4 bits are defined as the data code (8 = PLAY in the above example). <48H> The high level signal of 10 ms at the beginning of the DATA shows the start of the serial data. Data is determined by the length of L period. It is '0' when L is 5 ms, and '1' when 10 ms. The high level signal of 5 ms shows the separator of the data. The BUSY signal is inverted to high 1 ms faster than the start of DATA and goes low at the same time or later of the end (stop) of the DATA. The BUSY signal is used so that the DATA output signals do not interfere with each other between components, and should be low level when the serial data is output. If the BUSY signal is high, the data is output until the BUSY signal goes low.

4-2. System Connections with serial signals

Interface circuit

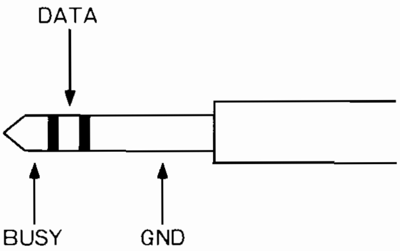
Both DATA and BUSY signals are connected as follows:



- Constants for R_1, R_2 differ for each microcomputer used.
  - In case of μPD7537, μPD7538..R_1: 4.7 kohms, R_2: 330 ohms
  - In other cases............R_1: 100 kohms, R_2: 680 ohms

Connection

DATA and BUSY lines should be connected using stereo mini jacks.

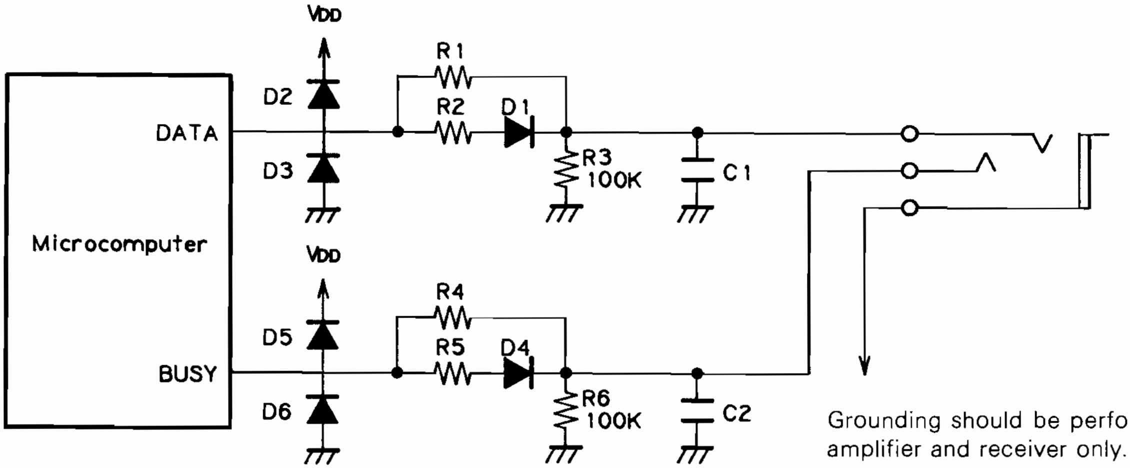


* GND should be applied to the amplifier.

Number of stereo mini jacks

Amplifier, Cassette Deck, Tuner, Remote Control	2
CD player, Turntable	1

CIRCUIT DESCRIPTION



Grounding should be performed for amplifier and receiver only.
In other cases, ground via capacitor or diode, or set open.

① R1, R4 :
R1, R4 are determined by the input leakage signal of the microcomputer used.

- When leakage current is less than 10 μA/5 V:
  R1, R4 = 100 kΩ
  (In the normal CMOS input port, it is almost 3 μA/5 V.)
- When the leakage current exceeds the above value:
  R1, R4 = 4.7 kΩ
  (Example : μPD77538AC/37AC, etc.)

② R2, R5 : For protection when output is shorted.

- The minimum value with which the current becomes the maximum rated value or less even when the output is short-circuited. But less the 680 Ω. Normally about 330 Ω.

③ R3, R6 : Input pull-down resistance.

- 100 kΩ regardless of the microcomputer. Be sure to insert the line between R1, D2 and the jacks.

④ D1, D4 : Reverse current prevention (To prevent the impedance from lowering when power is turned OFF.)

⑤ D7, D3, D5, D6 : For port protection. Be sure to insert this position (between R1, R4 and the port).

⑥ C1, C2 : For dealing with static electricity.

- It is attached only when required. Less than 1000 pF.

⑦ The GND terminal of the jacks should be grounded for the amplifier and the receiver only. For other components, it should be open, or grounded via capacitor or diodes.

⑧ When the input and output are separated such as using the N-channel open drain port, etc., it should be connected so as to be equivalent to the above circuit.

⑨ Since an impedance between R1, R4 and the microcomputer ports is high in the condition all components are connected, the connection line should be as short as possible.

CIRCUIT DESCRIPTION

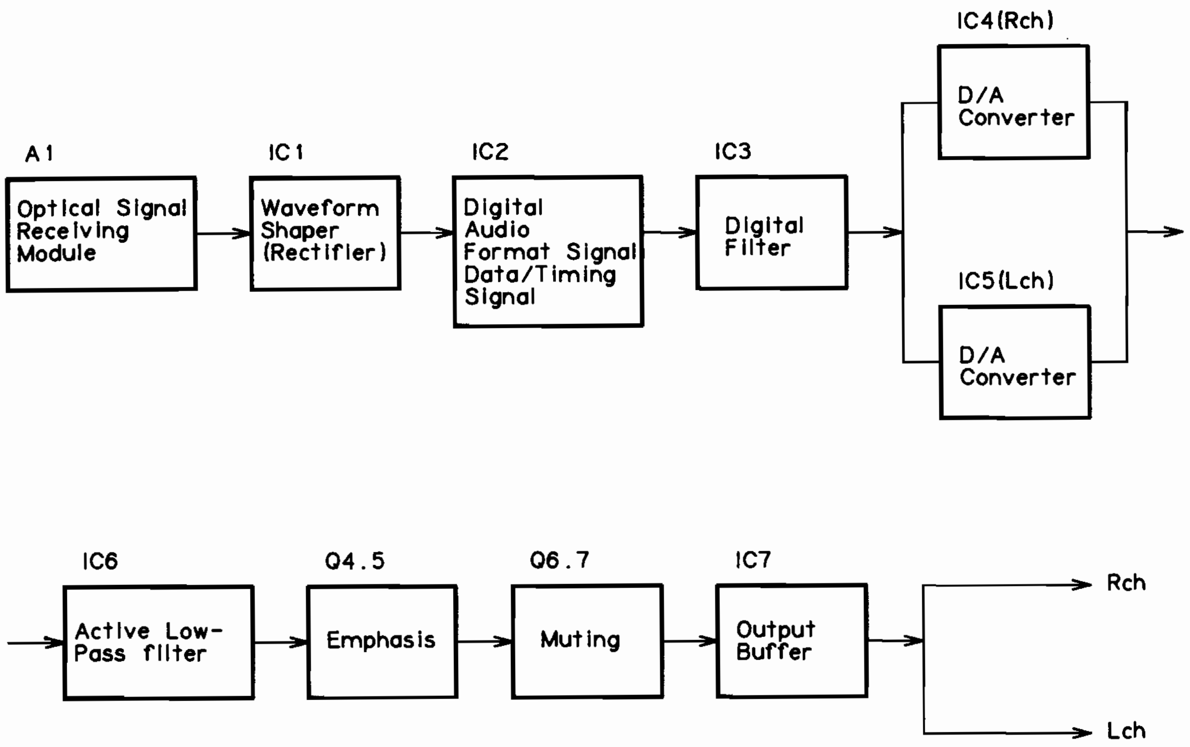
4-3. Data Contents List

FUNCTION Code (4-bit) / Type Code (4-bit)		AMPI	TUNER	PLAYER	CD	DECK A	DECK B	AMP II	REMOTE CONTROL							
	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	—	—	—	—	—	—	—	—	0	PHONO	DAT	FRONT BAL L	□ (PLAYER)	◁ (DECK A)	AV AMP M.READ 1	REC SEL TAPE1
1	—	POWER ON	POWER ON	POWER ON	POWER ON	POWER ON	AUTO BIAS START	—	1	TUNER	TV	FRONT BAL R	◁ or ◁/○ (PLAYER)	▷ (DECK A)	AV AMP M.READ 2	REC SEL TAPE 2
2	—	SEL.TUNER	Deck Timer P/R Cancel	—	—	TUNER REC	TUNER REC	POS. TUNER	2	CD (CD/AUX)	VIDEO 2	REAR BAL L	◁◁ (PLAYER)	◁◁ (DECK A)	AV AMP M.READ 3	REC SEL VIDEO 1
3	—	SEL. PHONO	—	—	—	PHONO REC	PHONO REC	POS PHONO	3	AUX (VIDEO 2)	(DIG1) DAT DIG.	REAR BAL R	▷▷ (PLAYER)	▷▷ (DECK A)	AV AMP M.READ 4	REC SEL VIDEO 2
4	—	SEL.CD	—	—	CLOSE	CD REC	CD REC	POS. CD	4	TAPE 1 (TAPE A)	(DIG 2) CD DIG.	VIDEO A (VDP1)	MODE (CD)	▯▯ (DECK A)	—	—
5	—	SEL. TAPE A	SYSTEM ON	COUNT	MEMORY READ	TAPE A REC	TAPE A REC	POS. TAPE A	5	TAPE 2 (TAPE B)	—	VIDEO B (VDP2)	GE	□ (DECK A)	—	—
6	—	SEL. TAPE B	SYSTEM OFF	MEMO	MEMO	TAPE B REC	TAPE B REC	POS.TAPE B	6	VIDEO (VIDEO 1)	—	VDP 3 (BS)	REAR VOL. DOWN	○ REC (DECK A)	—	—
7	—	SEL. AUX (SEL. VIDEO 3)	REMOCON KEY OFF	—	—	AUX REC	AUX REC	POS AUX	7	ROULETTE PLAY	—	AUDIO	REAR VOL. UP	SURROUND ON/OFF	—	—
8	—	ROULETTE ON	PLAY	PLAY	PLAY	PLAY	PLAY	—	8	CH. DOWN	DISC (CD CONTROL)	REAR BAL L	CHECK (CD)	◁ (DECK B)	SHARPNESS DOWN	—
9	—	ROULETTE OFF	—	STOP	STOP	STOP	STOP	—	9	CH. UP (PRESET SCAN)	—	REAR BAL R	□ (CD)	▷ (DECK B)	SHARPNESS UP	—
A	—	SEL. VIDEO (VIDEO1) (VTR)	SEL. ANALOG	MUTE ON	PAUSE	VIDEO REC	VIDEO REC	POS. VIDEO	VIDEO 3	VOL. DOWN	—	REAR VOL. DOWN	CLEAR (CD)	◁◁ (DECK B)	DETAIL DOWN	—
B	—	—	SEL. DIGITAL	MUTE OFF	SAMPLE START	—	Computer CD REC Start	PHONO DEC SW ON	MUSIC SELECT MODE ON/OFF	VOL. UP	—	REAR VOL. UP	▷ (CD)	▷▷ (DECK B)	DETAIL UP	—
C	—	—	DUB	DUB 2	DUB	—	DUB	PHONO REC SW OFF	MUSIC SELECT	MUTE	—	AV AMP MEMORY	REPEAT (CD)	▯▯ (DECK B)	DELAY TIME DOWN	—
D	—	—	SEL. CD DIG.	DUB 1	Tray OPEN	TAPE 2 REC	TAPE 2 REC	TAPE 2 ON	LW (J= TV)	POWER	+10 (CD)	AV AMP MEMU	EDIT (CD)	□ (DECK B)	DELAY TIME UP	—
E	—	—	SEL. DAT DIG.	RANDOM REC	Computer CD REC Standby	—	Computer CD REC Standby	TAPE 2 OFF	MW (AM)	DIRECT	TIME DISPLAY (CD)	TAPE MONITOR	|◁◁ (CD)	○ REC (DECK B)	ENHANCER ON/OFF	—
F	—	DP75 Connection Cord (SEL. DAT)	SEL. CDV.DIG.	—	S-CCRS START	POWER OFF (DAT REC)	DAY REC	POS. DAT	FM	PRESET FUNC. A/B	GE ON/OFF	AUDIO INJECTION	▷▷| (CD)	SURROUND MODE	DELAY ON/OFF	—

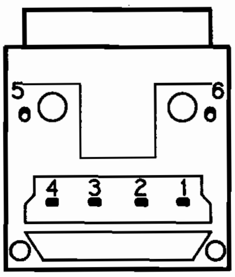
CIRCUIT DESCRIPTION

5-1. Signal Processing Unit (X32-1330-00)

Block Diagram


A1
Optical Signal Receiving Module
IC1
Waveform Shaper (Rectifier)
IC2
Digital Audio Format Signal Data/Timing Signal
IC3
Digital Filter
IC4(Rch)
D/A Converter
IC5(Lch)
D/A Converter
IC6
Active Low-Pass filter
Q4.5
Emphasis
Q6.7
Muting
IC7
Output Buffer
Rch
Lch

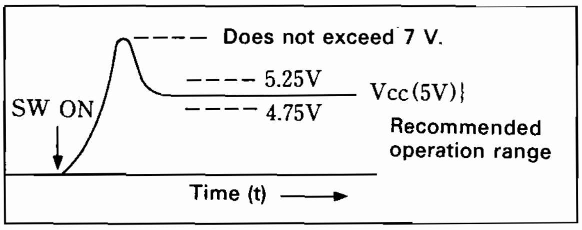
5-2. Optical Signal Receiving Module (W02-0937-05, TORX-174)



(Bottom View)

Pin No.	Connection
1	Output
2	GND
3	Vcc
4	GND
5	Case
6	Case

- **Power supply condition**



CIRCUIT DESCRIPTION

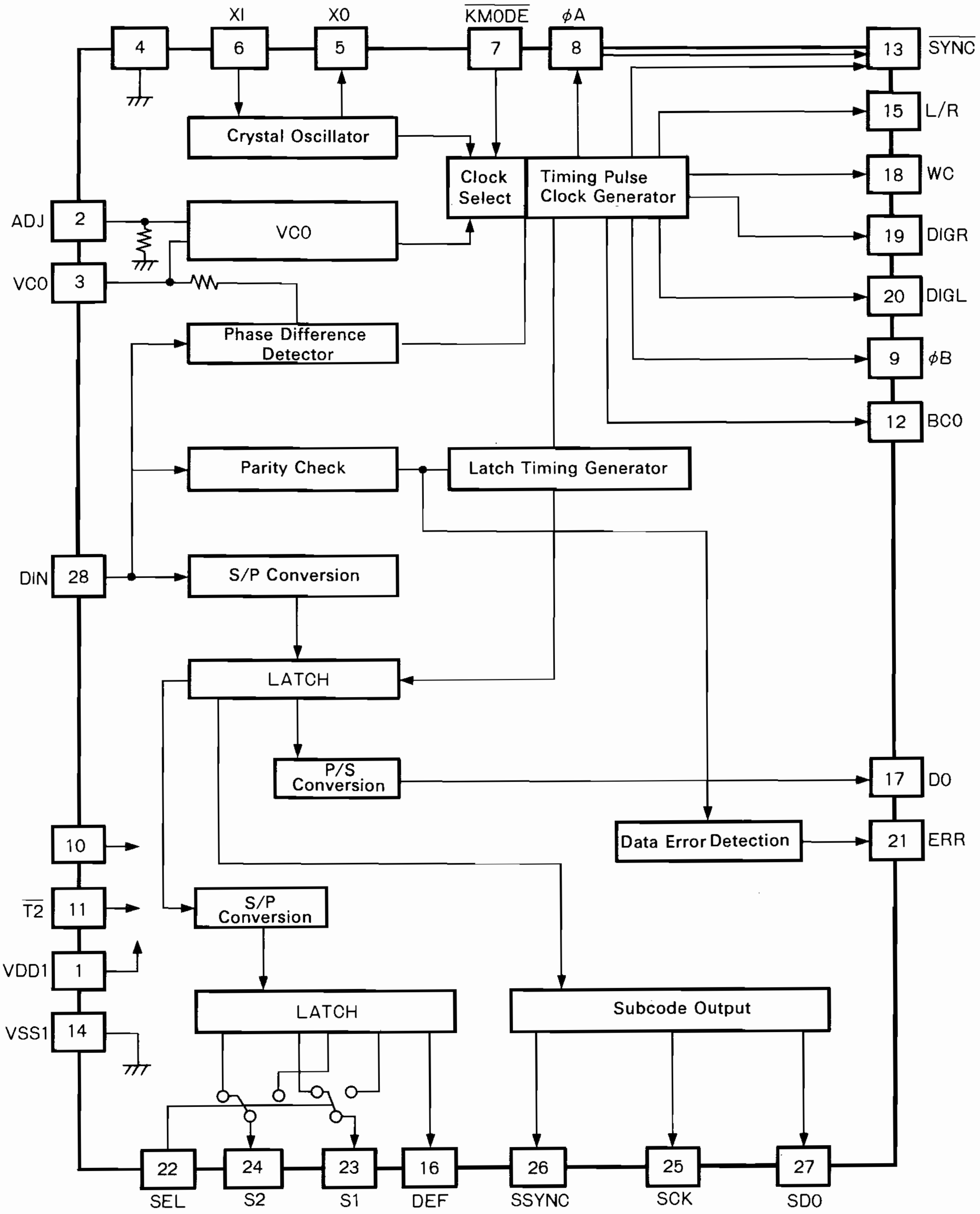
- **Electrical/optional characteristics (TORX-174)**

Type	Item	Symbol	Condition	Min. Value	Standard	Max. Value	Unit
TORX174	Maximum receiving photopower	P_{MAX}	6 Mb/s	-14.5	–	–	dBm
	Miximum receiving photopower	P_{MIN}	6 Mb/s	–	–	-24	dBm
	Rising time	tr	C_L=10pF	–	10	30	ns
	Faling time	tf	C_L=10pF	–	5	30	ns
	Power consumption	Icc		–	22	40	mA
	High level output voltage	V_{OH}	Vcc=4.75V, I_{OH}=-0.15mA	2.7	–	–	V
	Low level output voltage	V_{OL}	Vcc=4.75, I_{OL}=1.6mA	–	–	0.4	V

CIRCUIT DESCRIPTION

5-3. IC2 (YM3623B) X32-1330-00

Block Diagram


XI
XO
KMODE
ϕA
4
6
5
7
8
13
SYNC
Crystal Oscillator
15
L/R
Clock Select
Timing Pulse Clock Generator
18
WC
ADJ
2
VCO
19
DIGR
VCO
3
20
DIGL
Phase Difference Detector
9
ϕB
12
BCO
Parity Check
Latch Timing Generator
DIN
28
S/P Conversion
LATCH
P/S Conversion
17
DO
Data Error Detection
21
ERR
10
T2
11
S/P Conversion
VDD1
1
VSS1
14
LATCH
Subcode Output
22
SEL
24
S2
23
S1
16
DEF
26
SSYNC
25
SCK
27
SDO

CIRCUIT DESCRIPTION

IC2 (YM3623B)

- **Pin Functions**

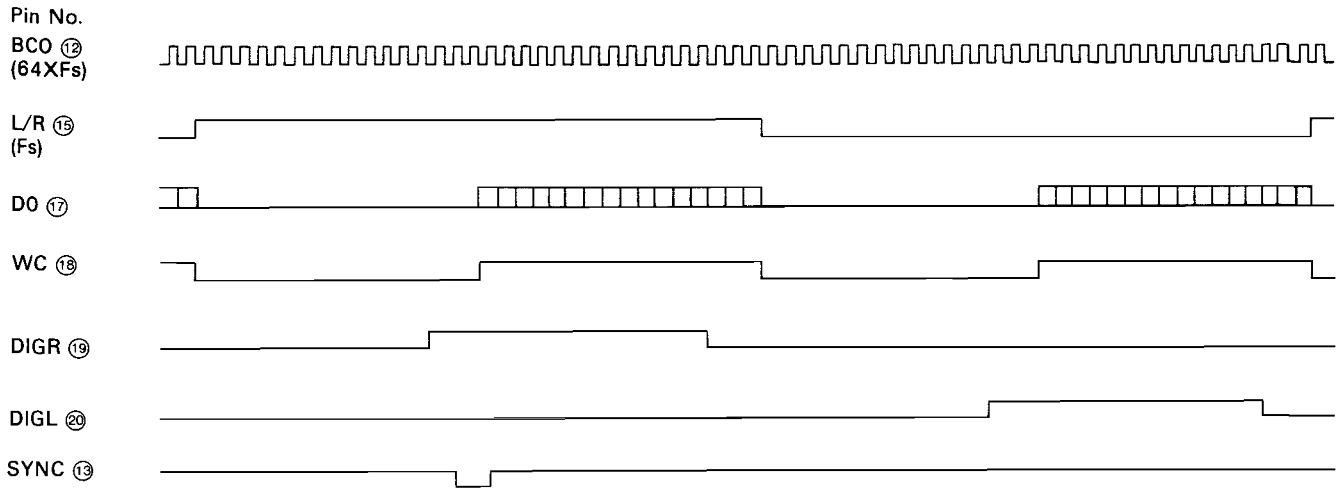
Pins with (PU) are pulled-up internally.

Pin No.	Pin Name	I/O	Name	Function
1	VDDI		VDD	System circuits power supply (+5 V).
2	ADJ	I	ADJ	VCO oscillator frequency adjustment pin. Non connection.
3	VCO	I/O	VCO	Externally connected capacitor pin for VCO circuit.
4	VSS2		Vss	GND pin for VCO circuit. Connect commonly with VSS1. They are commonly connected inside the LSI.
5	XO	O	XO	Crystal oscillator pin (16.9344 MHz).
6	XI	I	XI	Crystal oscillator pin.
7	$\overline{\text{KMODE}}$	I(PU)	KMODE	High : Activates the PLL circuit when the signal is input to DIN pin, and activates the crystal oscillator when no signal is input. Low: Uses the crystal oscillator regardless of the DIN pin.
8	ϕA	O	16.9M	16.9344 MHz when crystal oscillator is used. It varies depending on the data rate of the signal input to the DIN pin when the PLL circuit is activated. (About 16.9344 MHz when fs = 44.1 kHz.)
9	ϕB	O		Divided into 1/3 of øA when crystal oscillator is used. It varies depending on the data rate of the signal input to the DIN pin when the PLL circuit is activated. (About 5.6448 MHz when fs = 44.1 kHz)
10	$\overline{\text{T1}}$	I(PU)		Internal circuit check pin.
11	$\overline{\text{T2}}$	I(PU)		Internal circuit check pin.
12	BCO	O	BCLK	Timing clock of the signal output from DO pin.
13	$\overline{\text{SYNC}}$	O		Sync signal.
14	VSS1	O	Vss	GND for system circuit (+0 V).
15	L/R	O	LRCK	Indicates that the L or R channel data is output from DO pin. High = L channel, low = R channel.
16	DEF	O	EMPH	High = shows that emphasis is applied to the input data. Low = shows that emphasis is not applied to the input data.
17	DO	O	DATA	16-bit data output.
18	WC	O	WDCK	Shows that the data is output to DO pin.
19	DIGR	O		R-channel deglitch signal
20	DIGL	O		L-channel deglitch signal
21	ERR	O	ERR	High = parity error, or operates in crystal. Low = no error
22	SEL	I(PU)		Refer to separate section.
23	S1	O		Refer to separate section.
24	S2	O		Refer to separate section.
25	SCK	O		Subcode output clock.
26	SSYNC	O		Subcode signal.
27	SDO	O		Subcode data output pin.
28	DIN	I(PU)	DIN	Data input pin.

CIRCUIT DESCRIPTION

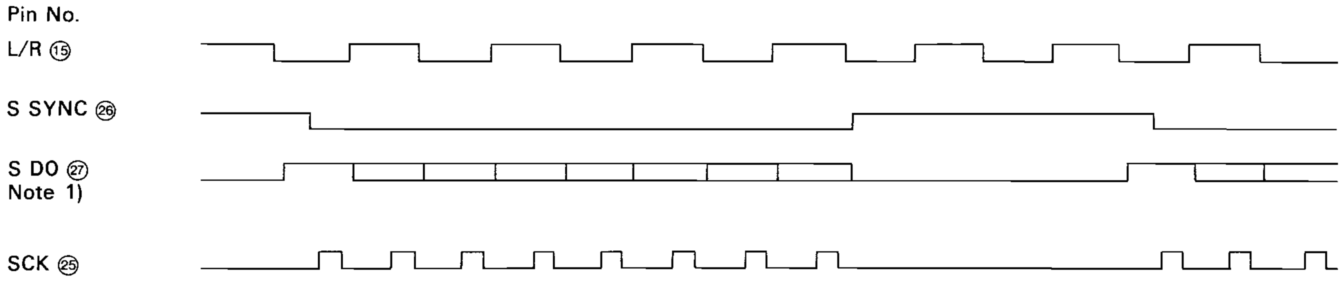
Timing Chart

Output timing ①


Pin No.
BCO ⑫ (64XFs)
L/R ⑮ (Fs)
DO ⑰
WC ⑱
DIGR ⑲
DIGL ⑳
SYNC ⑬

Note 1 : Fs shows the sampling frequency. For example, it is 44.1 kHz for the Compact Disc.

Output timing ②

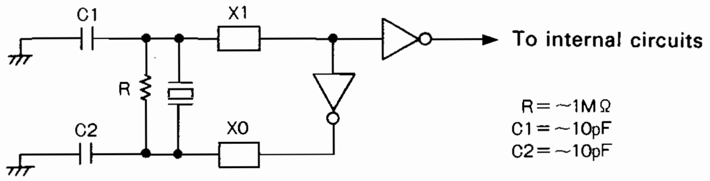

Pin No.
L/R ⑮
S SYNC ㉖
S DO ㉗ Note 1)
SCK ㉕

Note 1 : When the U bit input to DIN pin is '1', U bit will output to SDO (pin (27)) as it is for eight times.
Note 2 : The above timing shows a typical example. Depending on the data timing of the U bit input to DIN pin, SDO (pin (27)) data starts from L channel or R channel, and the period of one cycle for outputting 8 bits is varied.

CIRCUIT DESCRIPTION

Clock and Related Items

When a crystal oscillator (16.9344 MHz) is connected between pins X1 and X0, oscillation begins.

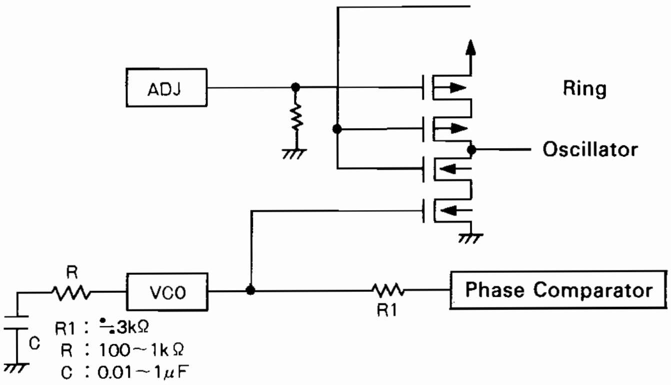


The crystal clock is operated when the $\overline{\text{KMODE}}$ pin is lowor when the $\overline{\text{KMODE}}$ pin is high and not data is input to DIN pin. At this time, all the data output to outside will be ignored. In the reverse condition as above, crystal oscillator is operated by the clock frequency which is picked up from the data input to the DIN pin with the internal PLL circuit.

Note : Since the $\overline{\text{KMODE}}$ pin is also used for the internal resetting, it should be inverted to low once at least once when the power is initially turned ON or before the PLL is locked to the input signal from the DIN pin. When inverting the KMODE pin to high, wait until the period exceeding the RC time constant for the VCO pin has been elapsed, and then set the $\overline{\text{KMODE}}$ pin to high.

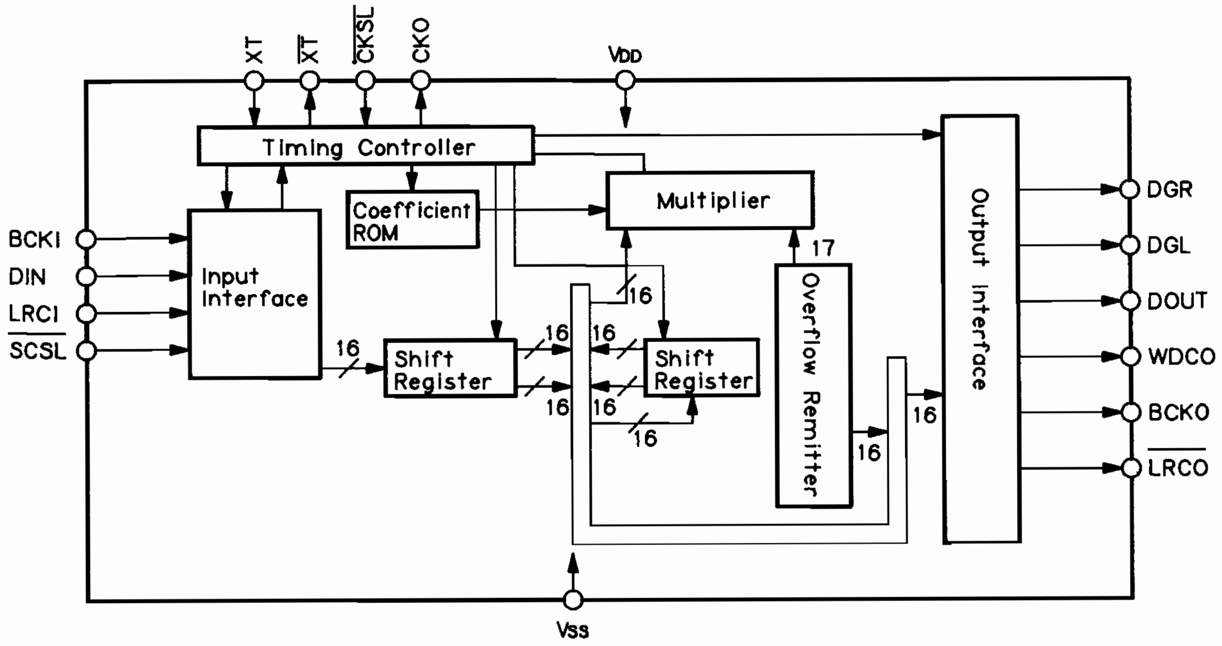
VCO and Related Items

To reproduce the clock frequency from the signal input to DIN pin, the PLL circuit is internally provided. The VCO pin is used for adjustment of the lead-in time constant. When the T1, T2 pins are set to low, KMODE pin to high and no data is input to DIN pin, the VCO starts oscillating with the self-running (free-running) frequency. No signal should be connected to ADJ pin. (No adjustment is required.)



CIRCUIT DESCRIPTION

5-4. IC3 (SM5807EP) Digital Filter


XT
$\overline{\text{XT}}$
$\overline{\text{CKSL}}$
CKO
VDD
Timing Controller
Coefficient ROM
Multiplier
Input Interface
Shift Register
Shift Register
Overflow Remitter
Output Interface
16
17
BCKI
DIN
LRCI
$\overline{\text{SCSL}}$
DGR
DGL
DOUT
WDCO
BCKO
$\overline{\text{LRCO}}$
Vss

Explanations of Pin

Pin No.	Pin Name	I/O	Pin Function
1	XT	I	Oscillator input pin.
2	$\overline{\text{XT}}$	o	Oscillator output pin when $\overline{\text{CKSL}}$ = high. (Open) when n $\overline{\text{CKSL}}$ = low
3	$\overline{\text{CKSL}}$	Ip	$\overline{\text{CKSL}}$ = high : 16.9344 MHz to XT.or 17.2872 MHz crystal oscillator or ecternal input. $\overline{\text{CKSL}}$ = low : 8.4672 MHz to XT or 8.6436 MHz external input.
4	CKO	o	Clock output (Buffer output of XT input clock)
5	LRCI	Ip	44.1 kHz sync clock input
6	DIN	Ip	Serial data input
7	BCKI	Ip	Serial input bit clock input
8	Vss		GND pin (0V)
9	$\overline{\text{SCSL}}$	Ip	$\overline{\text{SCSL}}$ = high : System clock = 192 fs $\overline{\text{SCSL}}$ = low : System colck = 196 fs (fs : sampling frequency)
10	DGR	o	R-channel deglitch signal (176.4 kHz)
11	DGL	o	L-channel deglitch signal (176.4 kHz)
12	DOUT	o	Serial data output
13	WDCO	o	Output control clock (352.8 kHz)
14	$\overline{\text{LRCO}}$	o	Output control clock (176.4 kHz)
15	BCKO	o	Serilal output bit clock (8.4672 MHz or 8.6436 MHz)
16	VDD		Power supply pin (5 V)

Note : I : Input pin (Ip shows that pull-up resistance is incorporated).
O : Output pin

CIRCUIT DESCRIPTION

■ Basic Concept of SM5807

● Basic structure of the filter

The SM5807 consists of a couple of linear phase FIR filters which each functioning as double oversampling operations are stacked serially, and its basic structure is as shown in Fig. 1. The input signal sampled with 44.1 kHz is converted into the signal sampled with 88.2 kHz by the primary 61st-order digital filter which operates a doubled oversampling frequency. Then the 13th-order secondary digital filter oversamples the input signal which is sampled by 88.2 kHz frequency with further doubled frequency. Thus, the originally input signal is quadruple oversampled and output in 176.4 kHz sampling rate. Fig. 2 simulates the filtering characteristics of each filter as well as the total characteristics by the combination of them. The 1st (primary) digital filter attenuates the frequencies over 24.1 kHz by 50 dB or 45 dB with the double-oversampling operation, as shown in (A). However, the sampling noise still remains in the range E24.1 kHz around 88.2 kHz frequency. When the 2nd (secondary) digital filter oversamples the output of the 1st digital filter in double oversampling operation, the sampling noise around 176.4 kHz remains but, the frequency range between 64.1 kHz and 112.3 kHz can be widely attenuated by setting the frequencies over 64.1 kHz as the rejection range, as shown in (B). In here, when the attenuation for the rejection range of the 2nd digital filter is set to 50 dB or 45 dB, the total characteristics by the stacked connection becomes as shown in (C), and thus the construction of the subsequent low-pass filter can be simply made. The actual filtering characteristics of the SM5807 which is designed in the above basic concept is shown in the section "filtering characteristics" on page 26.

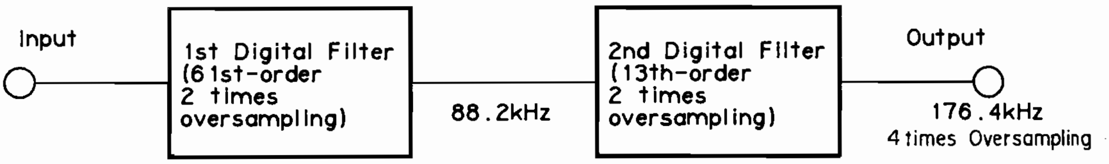

Input
1st Digital Filter (61st-order 2 times oversampling)
88.2kHz
2nd Digital Filter (13th-order 2 times oversampling)
Output
176.4kHz
4 times Oversampling

Fig.1

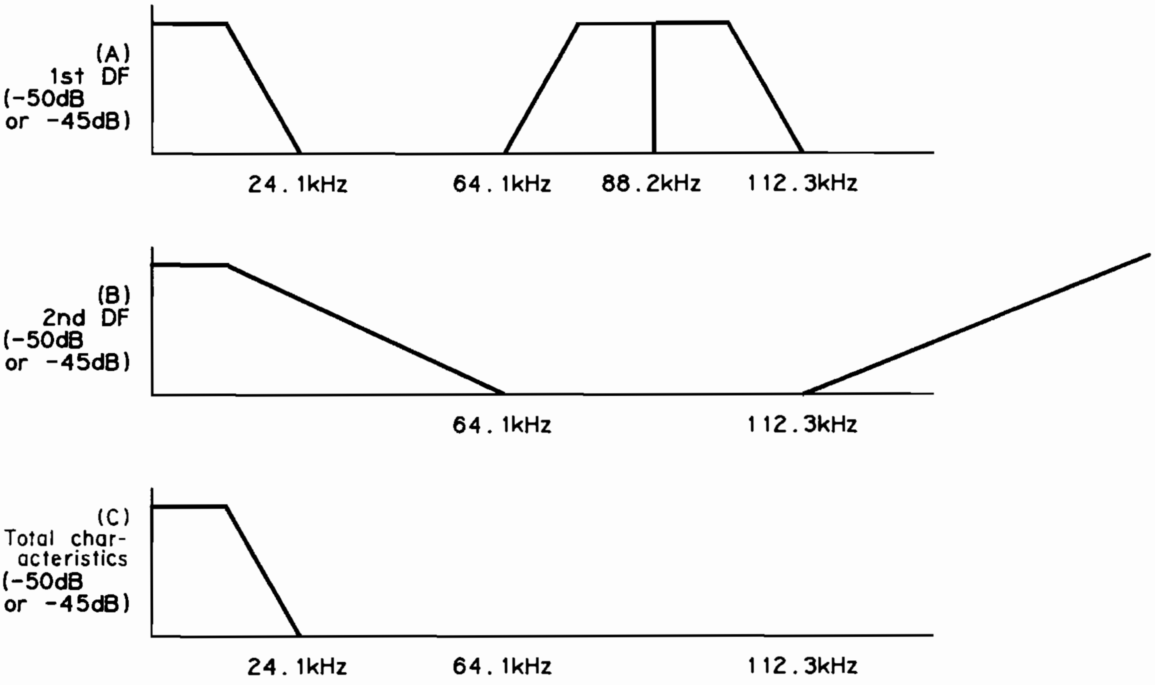

(A) 1st DF (-50dB or -45dB)
24.1kHz
64.1kHz
88.2kHz
112.3kHz
(B) 2nd DF (-50dB or -45dB)
64.1kHz
112.3kHz
(C) Total characteristics (-50dB or -45dB)
24.1kHz
64.1kHz
112.3kHz

Fig.2

CIRCUIT DESCRIPTION

Functions

(1) System clock

The system clock of this LSI is 192 fs = 8.4672 MHz or 196 fs = 8.6436 MHz. Either frequency can be selected by the $\overline{\text{SCSL}}$ pin. (fs = 44.1 kHz)

$\overline{\text{SCSL}}$ = high (or OPEN) .... 192 fs (corresponds to 96 fs)
$\overline{\text{SCSL}}$ = low .... 196 fs (corresponds to 98 fs)

- Clock generation method:

$\overline{\text{SCSL}}$	High (or open)		L	
$\overline{\text{CKSL}}$	High (or open)	L	High (or open)	L
Clock generating method	Crystal oscillator (X'tal)/ External input (EXT)	←	Crystal oscillator (X'tal)/ External input (EXT)	←
XT/$\overline{\text{XT}}$ input frequency	384fs=16.9344MHz	192fs=8.4672MHz	392fs=17.2872MHz	196fs=8.6436MHz
Clock output (CK$\overline{\text{O}}$)	↑	↑	↑	↑
System clock (internal)	192fs=8.4672MHz	↑	196fs=8.6436MHz	↑

(2) Serial input (Data and bit clock)

The serial input data is input at the rising edge of serial input bit clock BCKI. Therefore, the data is varied at the falling edge of BCKI. After 16-bit signal is input, the serial data is latched by the internal register by the sync clock LRCI.

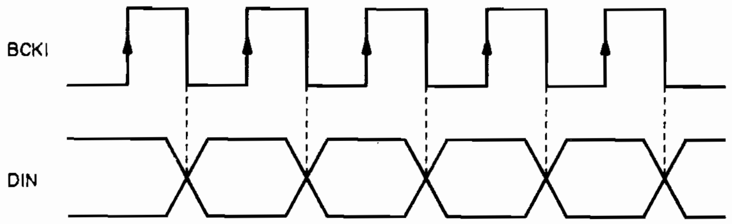


Fig. 3

(3) Input data

The data is input from MSB (most significant bit) first in 2's complement method.

(4) Operation start

Operation starts from the rising edge of the sync clock LRCI.

(5) Serial output (Data and bit clock)

Since the L-channel/R-channel serial data is output from DOUT alternately, the output data can be converted with a single DAC (D/A converter). At this time, when the externally connected circuits (two gates) are used, the in-phase conversion is made possible with two DAC units. The serial output bit clock is output from the BCKO pin. Since the data varies in synchronous with the falling edge of this bit clock, it can be input to the DAC at the rising edge.

BCKO = 8.4672 MHz .... (When $\overline{\text{SCSL}}$ is high or open: system clock = 192 fs)
BCKO = 8.6436 MHz .... (When $\overline{\text{SCSL}}$ is low: system clock = 196 fs)

(6) Output data

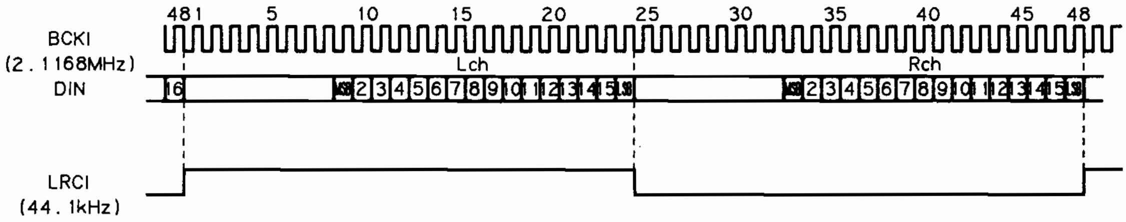
The 2's complement data is output from the MSb first.

(7) Deglitch signal

DGL L-ch deglitch signal : 176.4 kHz (25% duty)
DGR .... R-ch deglitch signal : 176.4 kHz (25% duty)

CIRCUIT DESCRIPTION

■ Timing Chart


BCKI
(2.1168MHz)
DIN
LRCI
(44.1kHz)
48 1 5 10 15 20 25 30 35 40 45 48
Lch
Rch
16
MSB 2 3 4 5 6 7 8 9 10 11 12 13 14 15 LSB

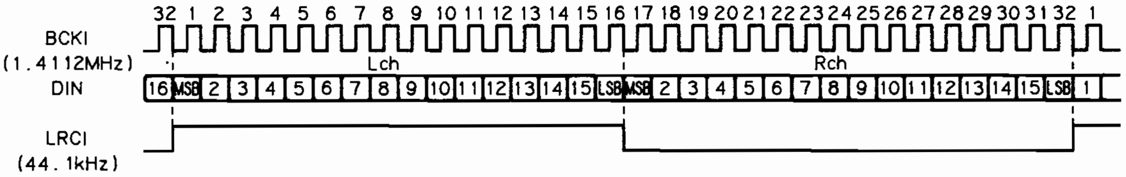

BCKI
(1.4112MHz)
DIN
LRCI
(44.1kHz)
32 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 1
Lch
Rch
16 MSB 2 3 4 5 6 7 8 9 10 11 12 13 14 15 LSB MSB 2 3 4 5 6 7 8 9 10 11 12 13 14 15 LSB 1

Fig.4 Serial input timing (when $\overline{\text{SCSL}}$ is high or open)

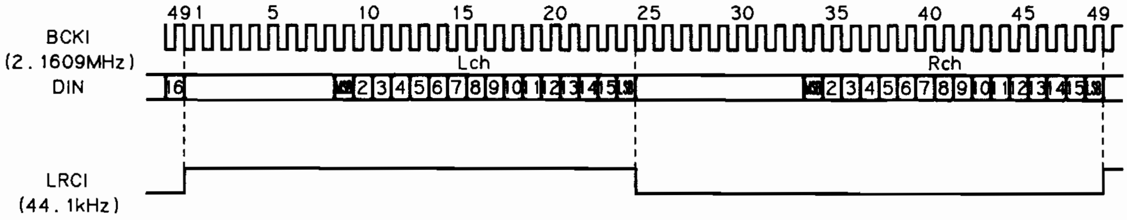

BCKI
(2.1609MHz)
DIN
LRCI
(44.1kHz)
49 1 5 10 15 20 25 30 35 40 45 49
Lch
Rch
16
MSB 2 3 4 5 6 7 8 9 10 11 12 13 14 15 LSB

Fig. 5 Serial input timing (when $\overline{\text{SCSL}}$ is low)

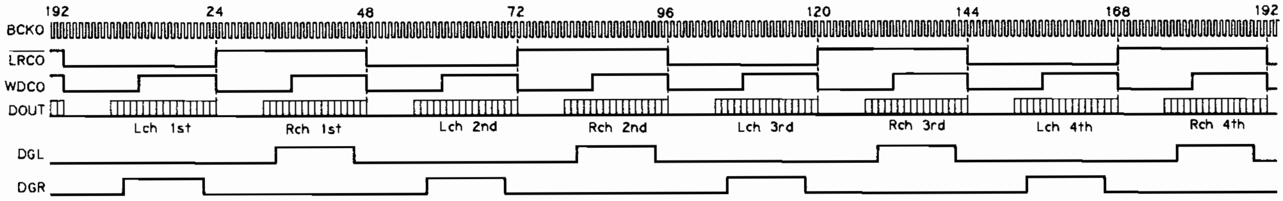

192 24 48 72 96 120 144 168 192
BCKO
LRCO
WDCO
DOUT
Lch 1st
Rch 1st
Lch 2nd
Rch 2nd
Lch 3rd
Rch 3rd
Lch 4th
Rch 4th
DGL
DGR

Fig. 6 Serial output timing (when $\overline{\text{SCSL}}$ is high or open)

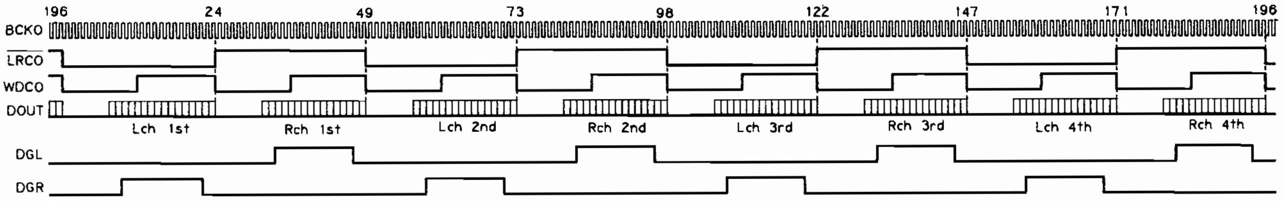

196 24 49 73 98 122 147 171 196
BCKO
LRCO
WDCO
DOUT
Lch 1st
Rch 1st
Lch 2nd
Rch 2nd
Lch 3rd
Rch 3rd
Lch 4th
Rch 4th
DGL
DGR

Fig. 7 Serial output timing (when $\overline{\text{SCSL}}$ is low)

Note : When SCSL is low, the WDCO output will not show the fixed period. However, DGL and DGR are fixed in 176.4 kHz (25% duty).

CIRCUIT DESCRIPTION

Characteristics of SM5807EP/ES

(Frequency 0 ~ 88.2 kHz)

Characteristics Item	Performance
Pass Band	0 ~ 20kHz
Rejection Band (Range)	Over 24.1 kHz
Band-pass Ripple	Within ± 0.05 dB
Rejection Range Attenuation	
(24.1K ~ 40.0kHz)	45 dB or more
(over 40.0 kHz)	50 dB or more

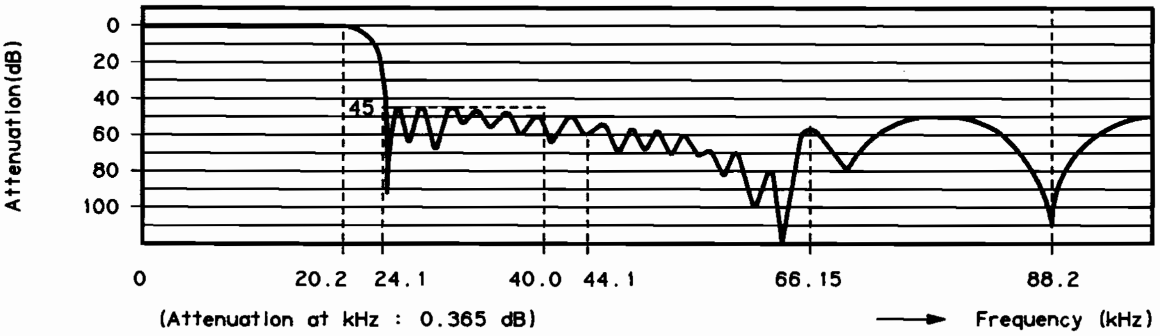


Fig. 8 SM5907EP/ES Frequency Response

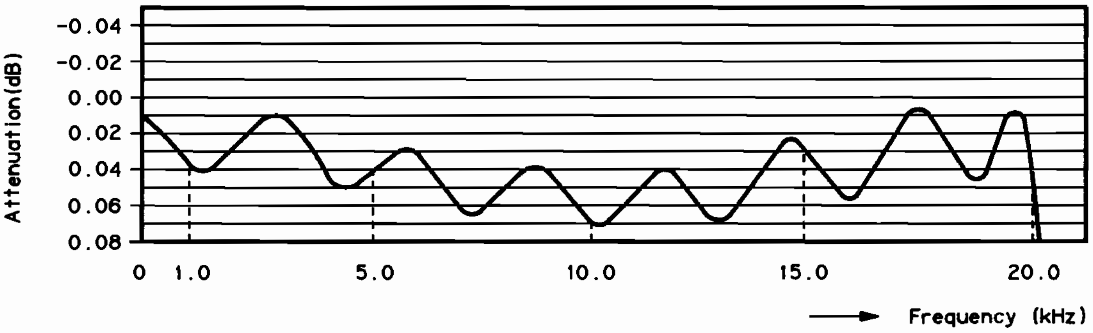


Fig. 9 SM5807EP/ES Pass-band Rejection Characteristics

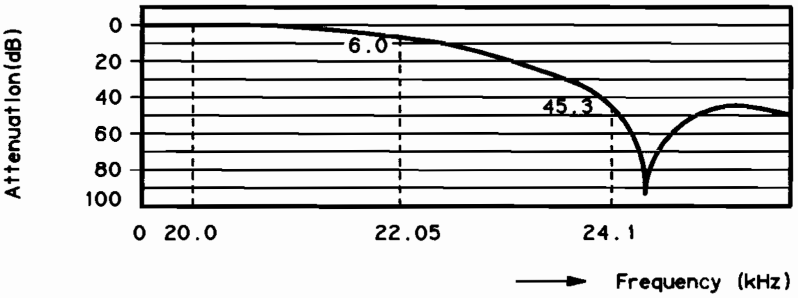


Fig. 10 S5807EP/ES Transition Band Characteristics

ADJUSTMENT/REGLAGE/ABGLEICH

ADJUSTMENT

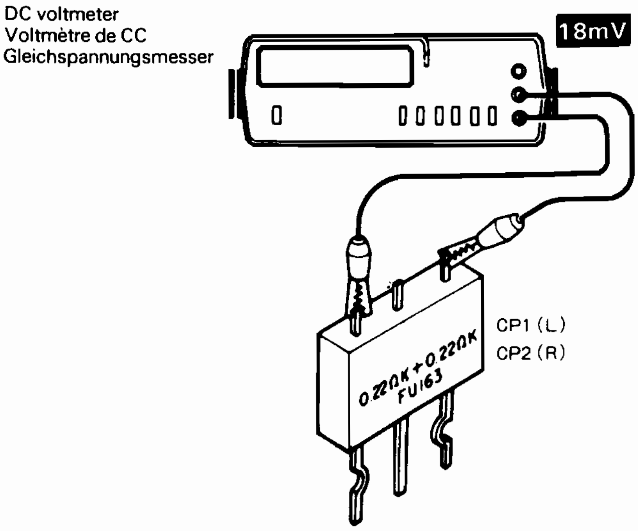
No.	ITEM	INPUT SETTINGS	OUTPUT SETTINGS	AMPLIFIER SETTINGS	ALIGNMENT POINTS	ALIGN FOR	FIG.
1	IDLE CURRENT	–	Connect a DC voltmeter across CP1 (L) CP2 (R) (X09-)	VOLUME: 0	VR1 (L) VR2 (R) (X89-)	18mV	(a)

REGLAGE

N°	ITEM	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DE L'AMPLIFICATEUR	POINT L'ALIGNEMENT	ALIGNER POUR	
1	COURANT DE POLARISATION	–	Connecter un voltmètre de CC sur CP1 (G) CP2 (D) (X09-)	VOLUME: 0	VR1 (G) VR2 (D) (X89-)	18mV	(a)

ABGLEICH

NR.	GEGENSTAND	EINGANGS-EINSTELLUNG	AUSGANGS-EINSTELLUNG	VERSTÄRKER EINSTELLUNG	ABGLEICH-PUNKTE	ABGLEICHEN FÜR	ABB.
1	LEERLAUFSTROM	–	Einen Gleichspannungs-messer über CP1 (L) CP2 (R) anschließen. (X09-)	VOLUME: 0	VR1 (L) VR2 (R) (X89-)	18mV	(a)


DC voltmeter
Voltmètre de CC
Gleichspannungsmesser
18mV
0
000000
0.22ΩK+0.22ΩK
FU163
CP1 (L)
CP2 (R)

(a)

VOLTAGE CHECK TABLE

(X00-252X-XX)

	B	C	E
Q1	1.5V	ON 2.1V OFF −2.1V	2.1V
Q2	1.5V	—	2.1V
Q4	ON 2.1V OFF −2.1V	—	—
Q5	—	—	ON 2.1V OFF −2.1V
Q6	ON 2.1V OFF −2.1V	—	—
Q7	ON 2.1V OFF −2.1V	—	—
Q8	ON 2.1V OFF −2.1V	—	—

IC1

1	12.5V
2	0.6V
3	5.7V

(X09-273X-XX)

	B	C	E
Q1	0.6V	—	0V
Q2	0.6V	—	0V
Q3	0.6V	−4.2V	0V
Q4	−0.6V	−4.2V	0V
Q5	−0.6V	0V	−1.2V
Q6	−0.6V	0V	−1.2V
Q7	−0.6V	−13V	—
Q8	−13V	−22V	−12.4V

IC1

1	−12V
28	11.5V

IC2

1	−12V
28	11.5V

IC4

1	0V
2	—
3	0V
4	−12.4V
5	0V
6	0V
7	0V
8	12V

IC5

1	0V
2	0V
3	0V
4	−12.4V
5	0
6	0
7	0
8	12

(X11-2560-00)

IC1

2	5V
64	5V

IC4

1	0.7V
2	0.6V
3	0V
4	0V
5	0V
6	0V
7	11.7V
8	11.7V
9	0.7V
10	0.6V

IC5

1	0V
2	0V
3	−16.8V
4	0V
5	16.8V

IC7

1	0V
2	0V
3	0V
4	−12V
5	0V
6	0V
7	0V
8	12V

(X13-5800-00)

IC1

1	0V	5	0V
2	0V	6	0V
3	0V	7	0V
4	−12V	8	12V

(X-13-6140-00)

IC1

7	−13V
14	12V

(X32-1330-00)

	B	C	E
Q9	—	−12V	−5
Q10	—	12V	5V
Q11	ON 5V OFF 4.4V	—	—

IC2

1	5V

IC4

1	−5V
16	5V

IC5

1	−5V
3	5V
8	−5V
16	5V

IC6

1	0V
2	0V
3	—
4	12V
5	—
6	0V
7	0V
8	0V
9	0V
10	—
11	−12V
12	—
13	0V
14	0V

IC7

1	0V
2	—
3	—
4	−12V
5	—
6	—
7	0V
8	12V

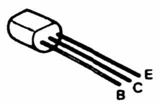
(X85-103X-XX)

IC1

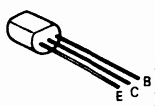
1	0V	5	0V
2	0V	6	0V
3	0V	7	0V
4	−12V	8	12V

(X89-101X-XX)

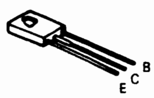
	B	C	E		B	C	E
Q1	0V	−38.0V	0.7V	Q11	1.2V	42.0V	0.6V
Q2	—	—	—	Q12	—	—	—
Q3	0	−38.0V	0.7V	Q13	−1.2V	−42.0V	−0.6V
Q4	—	—	—	Q14	—	—	—
Q5	−38.0V	20.8V	−38.6V	Q15	—	—	—
Q6	—	—	—	Q16	—	—	—
Q7	−38.0V	—	−38.6V	Q17	12.0V	−12.0V	12.0V
Q8	—	—	—	Q18	—	12.0V	—
Q9	39.4V	—	40.0V	Q19	11.8V	12.0V	11.2V
Q10	—	—	—				



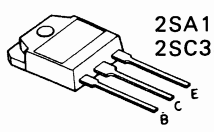
DTA124EN



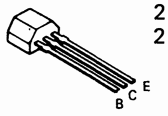
DTC124EN
2SA7332(A)
2SA954
2SA992
2SA999
2SC1845
2SC1923
2SC2003
2SC2320
2SC2878
2SC945(A)
2SD1302



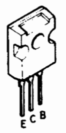
2SA1110
2SC2590



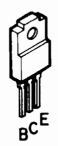
2SA149*5
2SC3855*5



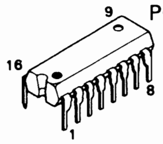
2SA933S
2SC1740S



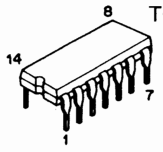
2SC4137



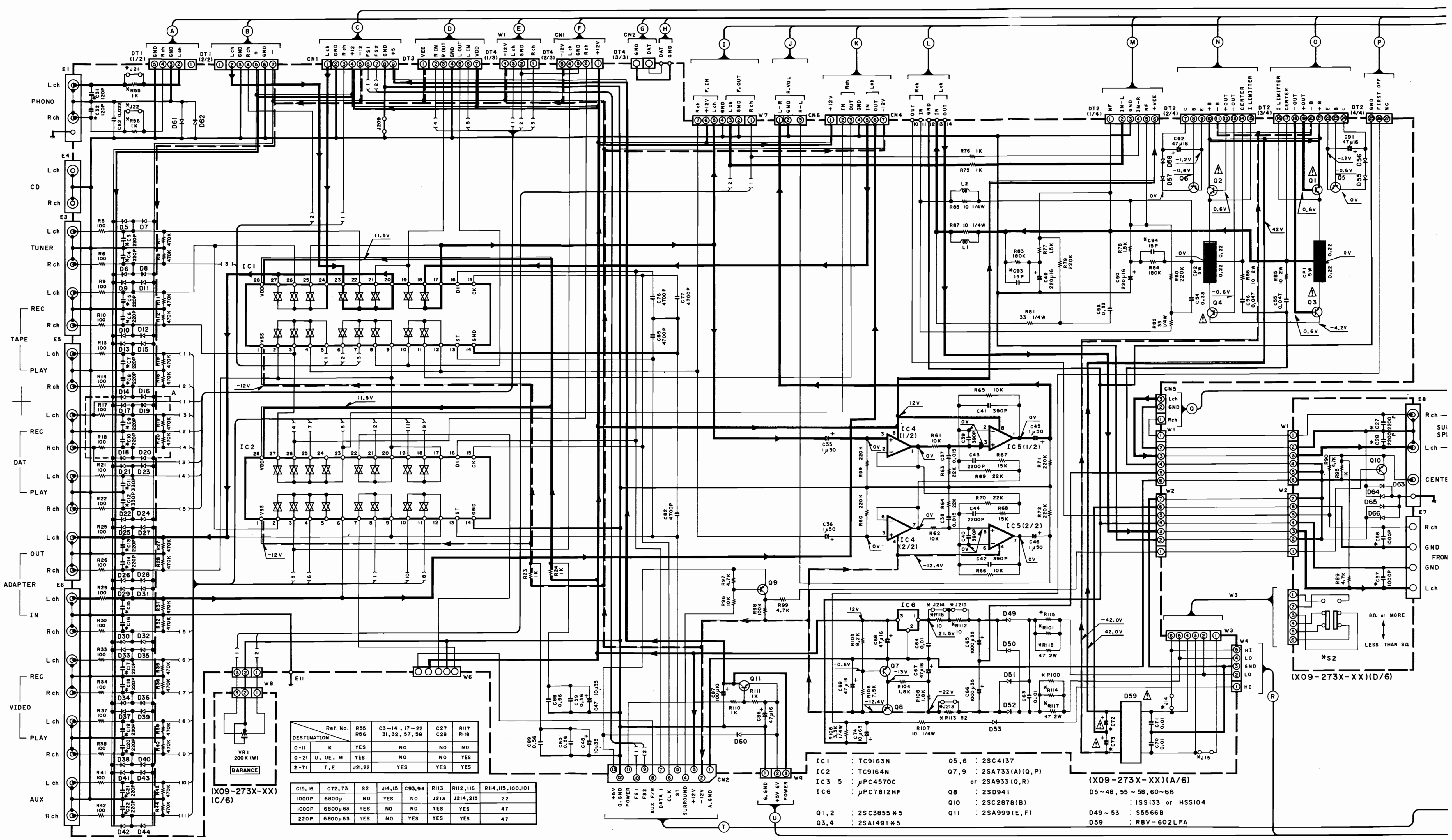
2SB941



PCM56P-L



TC74HC00P


PHONO
Lch
Rch
CD
TUNER
TAPE
REC
PLAY
DAT
ADAPTER
OUT
IN
VIDEO
AUX
IC1
IC2
IC4(1/2)
IC4(2/2)
IC5(1/2)
IC5(2/2)
IC6
Q1
Q2
Q3
Q4
Q7
Q8
Q9
Q10
Q11
D59
VR1
200K (W)
BARANCE
CN1
CN2
CN4
CN5
CN6
DT1
DT2
DT3
DT4
E1
E3
E4
E5
E6
E7
E8
E11
W1
W2
W3
W4
W6
W7
W8
W9
*S2
8Ω or MORE
LESS THAN 8Ω
(X09-273X-XX)(C/6)
(X09-273X-XX)(A/6)
(X09-273X-XX)(D/6)
CENTER
SURROUND
Rch
Lch
GND
FRONT

DESTINATION \ Ref. No.		R55 R56	C3~14 , 17~22 31, 32, 57, 58	C27 C28	R117 R118
0-11	K	YES	NO	NO	NO
0-21	U, UE, M	YES	NO	NO	YES
2-71	T, E	J21,22	YES	YES	YES

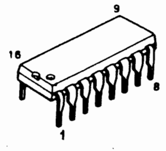
C15, 16	C72, 73	S2	J14, 15	C93, 94	R113	R112, 116	R114, 115, 100, 101
1000P	6800μ	NO	YES	NO	J213	J214, 215	22
1000P	6800μ63	YES	NO	NO	YES	YES	47
220P	6800μ63	YES	NO	YES	YES	YES	47

IC1 : TC9163N
IC2 : TC9164N
IC3 5 : μPC4570C
IC6 : μPC7812HF

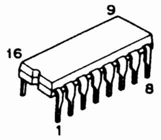
Q1, 2 : 2SC3855*5
Q3, 4 : 2SA1491*5

Q5, 6 : 2SC4137
Q7, 9 : 2SA733(A)(Q, P)
or 2SA933(Q, R)
Q8 : 2SD941
Q10 : 2SC2878(B)
Q11 : 2SA999(E, F)

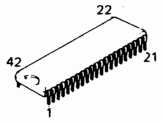
D5~48, 55~58, 60~66
: 1SS133 or HSS104
D49~53 : S5566B
D59 : RBV-602LFA



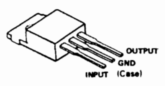
SM5807EP



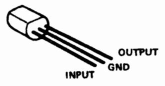
UPC4574C



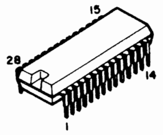
AN6556
M5218P
UPC4570C


OUTPUT
GND
INPUT (Case)

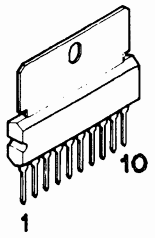
UPC7805HF
UPC7812HF


OUTPUT
GND
INPUT

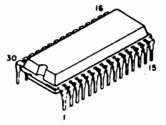
M51951ASL



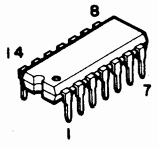
YM3623B



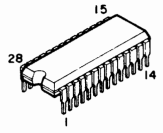
BA6109



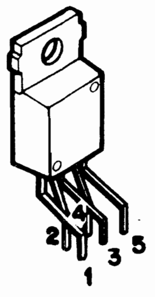
M50461-057SP



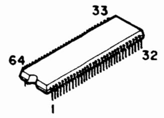
LC4966



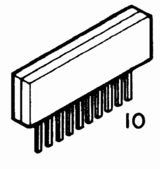
TC9163N
TC9164N



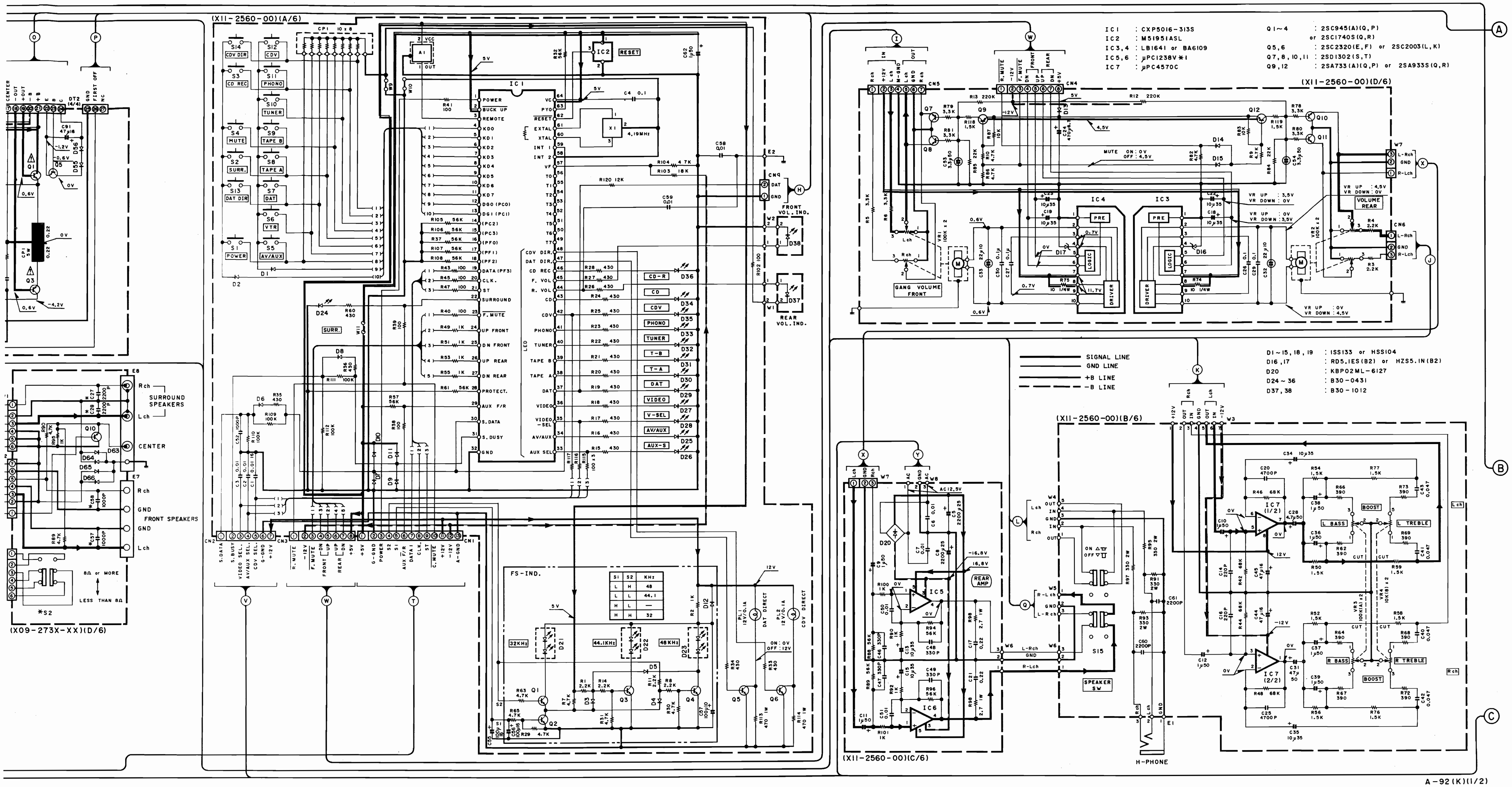
UPC1238V*1



CXP5016-315S



LB1641


(X11-2560-00)(A/6)
(X11-2560-00)(B/6)
(X11-2560-00)(C/6)
(X11-2560-00)(D/6)
(X09-273X-XX)(D/6)
IC1 : CXP5016-313S
IC2 : M51951ASL
IC3,4 : LB1641 or BA6109
IC5,6 : µPC1238V ✱1
IC7 : µPC4570C
Q1~4 : 2SC945(A)(Q,P) or 2SC1740S(Q,R)
Q5,6 : 2SC2320(E,F) or 2SC2003(L,K)
Q7,8,10,11 : 2SD1302(S,T)
Q9,12 : 2SA733(A)(Q,P) or 2SA933S(Q,R)
D1~15,18,19 : 1SS133 or HSS104
D16,17 : RD5.1ES(B2) or HZS5.1N(B2)
D20 : KBP02ML-6127
D24~36 : B30-0431
D37,38 : B30-1012
SIGNAL LINE
GND LINE
+B LINE
−B LINE
SURROUND SPEAKERS
CENTER
FRONT SPEAKERS
8Ω or MORE
LESS THAN 8Ω
✱S2
FRONT VOL. IND.
REAR VOL. IND.
FS-IND.
GANG VOLUME FRONT
VOLUME REAR
SPEAKER SW
H-PHONE
REAR AMP
L BASS
L TREBLE
R BASS
R TREBLE
BOOST
A-92(K)(1/2)

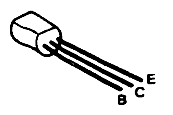
CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). ⚠ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returend to the customer.

- DC voltages are measured with a high impedance voltmeter. Values may vary slightly due to variations between in dividual instruments or/and units.
- Les tensions c.c. doivent être measurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.
- Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Voltmeter gemessen. Dabei schwanken die Meßwerte aufgrunt von Unterschieden zwischen einzelnen Instrumenten oder Geräten u. geringfügig.

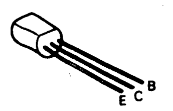
Y08-3560-11

A-92

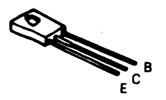
KENWOOD



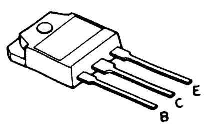
DTA124EN



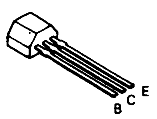
DTC124EN
2SA7332(A)
2SA954
2SA992
2SA999
2SC1845
2SC1923
2SC2003
2SC2320
2SC2878
2SC945(A)
2SD1302



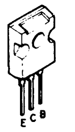
2SA1110
2SC2590



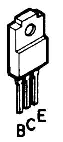
2SA149*5
2SC3855*5



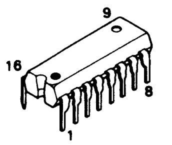
2SA933S
2SC1740S



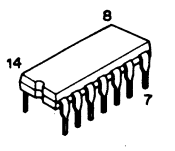
2SC4137



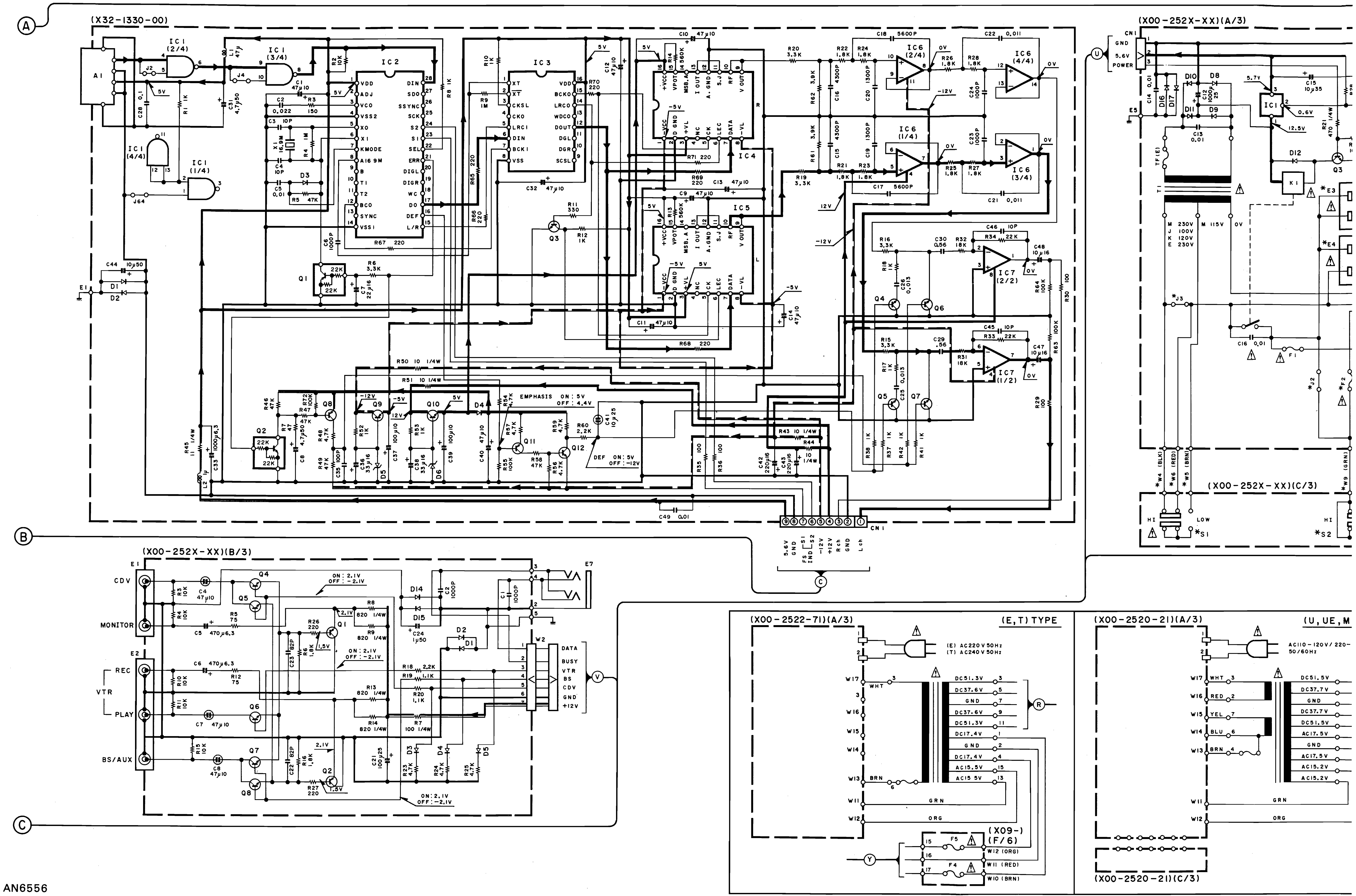
2SB941



PCM56P-L

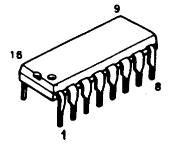


TC74HC00P

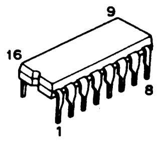


A
B
C
(X32-1330-00)
IC1 (2/4)
IC1 (3/4)
IC1 (4/4)
IC1 (1/4)
IC2
IC3
IC4
IC5
IC6 (2/4)
IC6 (4/4)
IC6 (1/4)
IC6 (3/4)
IC7 (2/2)
IC7 (1/2)
A1
J2
J4
J64
E1
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q9
Q10
Q11
Q12
VDD
ADJ
VCO
VSS2
XO
XI
KMODE
A16 9M
B
T1
T2
BCO
SYNC
VSS1
DIN
SDO
SSYNC
SCK
S2
SI
SEL
ERRO
DIGL
DIGR
WC
DO
DEF
L/R
XT
CKSL
CKO
LRCI
BCKI
VSS
BCKO
LRCO
WDCO
DOUT
DGL
DGR
SCSL
+VCC
VPOT
MSB.A
I OUT
A.GND
S.J
RF
V OUT
D GND
+VL
NC
CK
LEC
DATA
-VL
EMPHASIS ON: 5V OFF: 4.4V
DEF ON: 5V OFF: -12V
CN1
5.6V
GND
FS
IND S1
S2
-12V
+12V
Rch
GND
Lch
(X00-252X-XX)(A/3)
5.6V POWER
D10
D8
D16
D17
D11
D9
D12
TF(E)
T1
M 230V
J 100V
K 120V
E 230V
M 115V
0V
K1
*E3
*E4
*J3
*J2
*F2
F1
*W4 (BLK)
*W6 (RED)
*W5 (BRN)
*W9 (GRN)
(X00-252X-XX)(C/3)
HI
LOW
*S1
*S2
(X00-252X-XX)(B/3)
E1
E2
E7
CDV
MONITOR
REC
VTR
PLAY
BS/AUX
ON: 2.1V OFF: -2.1V
W2
DATA
BUSY
VTR
BS
CDV
GND
+12V
V
D14
D15
(X00-2522-71)(A/3)
(E, T) TYPE
(E) AC220V 50Hz
(T) AC240V 50Hz
W17
W16
W15
W14
W13
W11
W12
WHT
BRN
GRN
ORG
DC51.3V
DC37.6V
GND
DC17.4V
AC15.5V
AC15 5V
R
(X09-)(F/6)
F5
F4
W12 (ORG)
W11 (RED)
W10 (BRN)
Y
(X00-2520-21)(A/3)
(U, UE, M
AC110~120V/220~
50/60Hz
RED
YEL
BLU
DC51.5V
DC37.7V
DC51.5V
AC17.5V
AC15.2V
(X00-2520-21)(C/3)

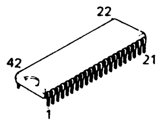
SM5807EP



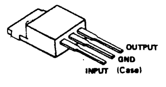
UPC4574C



AN6556
M5218P
UPC4570C

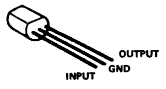


UPC7805HF
UPC7812HF



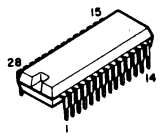
OUTPUT
GND
INPUT (Case)

M51951ASL

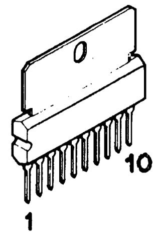


OUTPUT
GND
INPUT

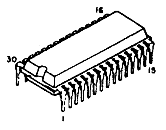
YM3623B



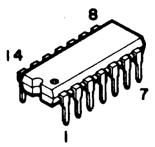
BA6109



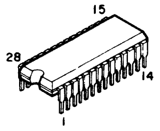
M50461-057SP



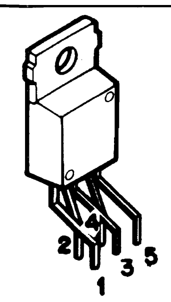
LC4966



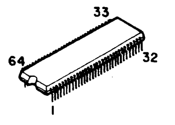
TC9163N
TC9164N



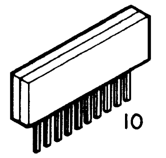
UPC1238V*1

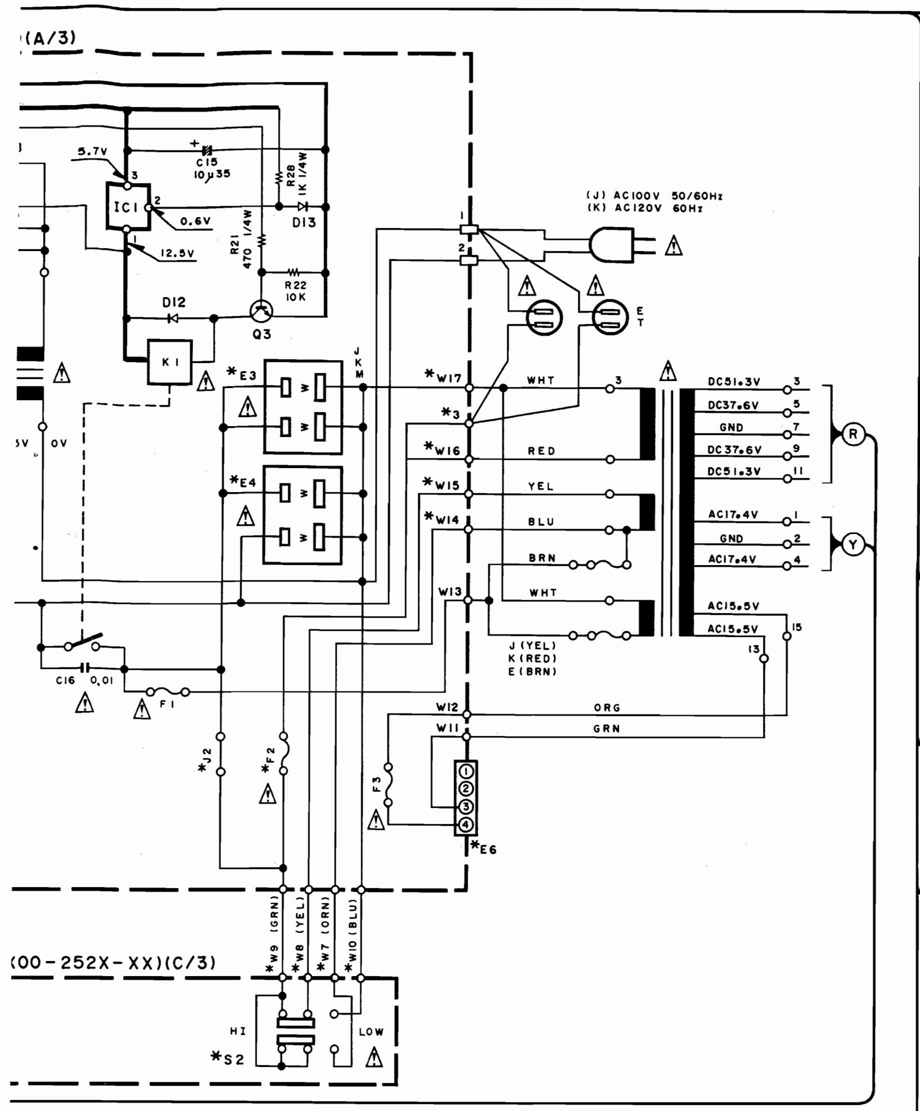


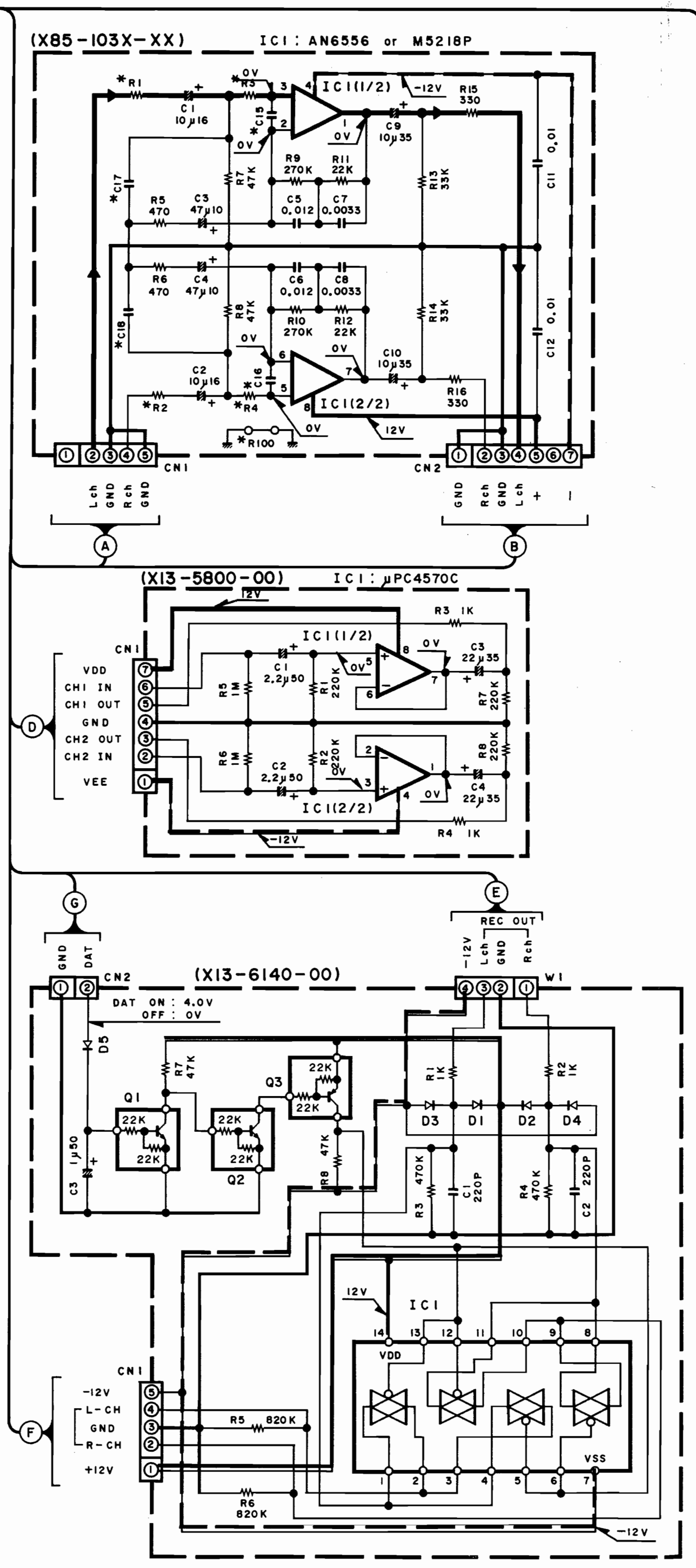
CXP5016-315S

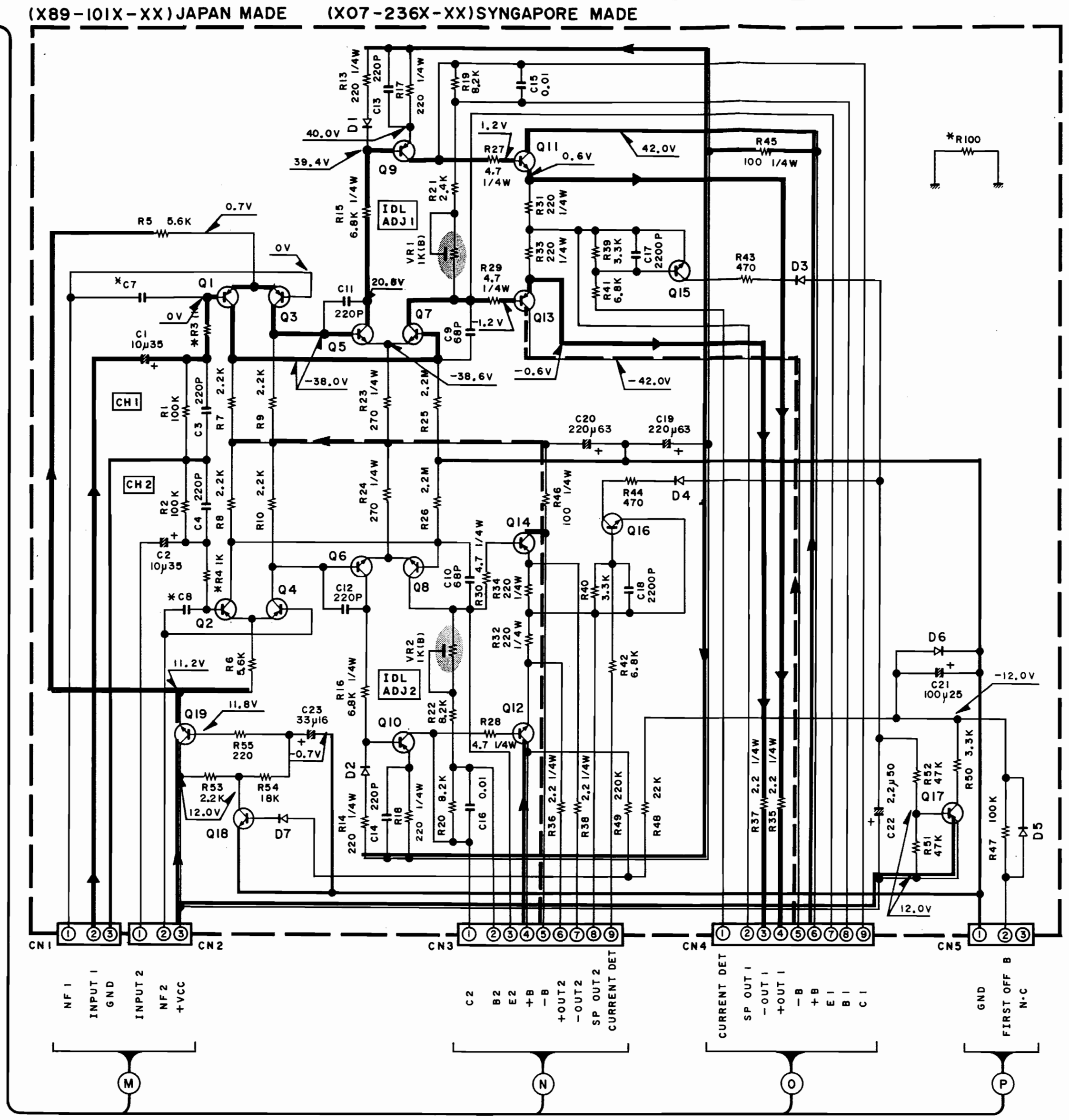


LB1641




(A/3)
IC1
5.7V
0.6V
12.5V
C15 10μ35
R28 1K 1/4W
R21 470 1/4W
D13
D12
R22 10K
Q3
K1
*E3
*E4
C16 0.01
F1
*J2
*F2
F3
(J) AC100V 50/60Hz
(K) AC120V 60Hz
*W17
*3
*W16
*W15
*W14
W13
W12
W11
WHT
RED
YEL
BLU
BRN
WHT
ORG
GRN
J (YEL)
K (RED)
E (BRN)
DC51.3V
DC37.6V
GND
DC37.6V
DC51.3V
AC17.4V
GND
AC17.4V
AC15.5V
AC15.5V
*E6
*W9 (GRN)
*W8 (YEL)
*W7 (ORN)
*W10 (BLU)
(00-252X-XX)(C/3)
HI
LOW
*S2


(X85-103X-XX)
IC1 : AN6556 or M5218P
*R1
C1 10μ16
*R3
*C15
IC1(1/2)
-12V
R15 330
C9 10μ35
*C17
R5 470
C3 47μ10
R7 47K
R9 270K
R11 22K
C5 0.012
C7 0.0033
R13 33K
C11 0.01
R6 470
C4 47μ10
R8 47K
C6 0.012
C8 0.0033
R10 270K
R12 22K
R14 33K
C12 0.01
*C18
C2 10μ16
*C16
C10 10μ35
R16 330
*R2
*R4
IC1(2/2)
*R100
12V
0V
CN1
CN2
Lch GND Rch GND
GND Rch GND Lch + -
A
B
(X13-5800-00)
IC1 : μPC4570C
12V
R3 1K
IC1(1/2)
C3 22μ35
C1 2.2μ50
R5 1M
R1 220K
R7 220K
R6 1M
C2 2.2μ50
R2 220K
R8 220K
C4 22μ35
IC1(2/2)
R4 1K
-12V
CN1
VDD
CH1 IN
CH1 OUT
GND
CH2 OUT
CH2 IN
VEE
D
G
E
GND DAT
CN2
(X13-6140-00)
DAT ON : 4.0V
OFF : 0V
D5
R7 47K
Q1
Q2
Q3
22K
C3 1μ50
R8 47K
REC OUT
-12V Lch GND Rch
W1
R1 1K
R2 1K
D3 D1 D2 D4
R3 470K
C1 220P
R4 470K
C2 220P
12V
IC1
VDD
VSS
CN1
-12V
L-CH
GND
R-CH
+12V
R5 820K
R6 820K
F


(X89-101X-XX)JAPAN MADE
(X07-236X-XX)SINGAPORE MADE
R13 220 1/4W
C13 220P
R17 220 1/4W
R19 8.2K
R18
C15 0.01
40.0V
39.4V
1.2V
R27
Q11
0.6V
42.0V
R45 100 1/4W
*R100
Q9
4.7 1/4W
R21 2.4K
R15 6.8K 1/4W
IDL ADJ1
VR1 1K(B)
R31 220 1/4W
R33 220 1/4W
R39 3.3K
C17 2200P
Q15
R41 6.8K
R43 470
D3
R5 5.6K
0.7V
0V
*C7
Q1
Q3
C11 220P
20.8V
R29 4.7 1/4W
Q13
Q7
C9 68P
1.2V
0V
Q5
-38.0V
-38.6V
-0.6V
-42.0V
C1 10μ35
*R3
CH1
R1 100K
C3 220P
R7 2.2K
R9 2.2K
R23 270 1/4W
R25 2.2K
C20 220μ63
C19 220μ63
CH2
R2 100K
C4 220P
R8 2.2K
R10 2.2K
R24 270 1/4W
R26 2.2M
R46 100 1/4W
R44 470
D4
Q14
Q16
C2 10μ35
*R4 1K
Q6
C10 68P
R30 4.7 1/4W
R34 220 1/4W
R40 3.3K
C18 2200P
*C8
Q2
Q4
C12 220P
Q8
11.2V
R6 5.6K
R16 6.8K 1/4W
IDL ADJ2
VR2 1K(B)
R32 220 1/4W
R42 6.8K
D6
C21 100μ25
-12.0V
Q19
11.8V
R55 220
C23 33μ16
-0.7V
Q10
R22 8.2K
R28 4.7 1/4W
Q12
R53 2.2K
R54 18K
12.0V
D2
Q18
D7
R14 220 1/4W
C14 220P
R20 220 1/4W
C16 0.01
R36 2.2 1/4W
R38 2.2 1/4W
R49 220K
R48 22K
R37 2.2 1/4W
R35 2.2 1/4W
C22 2.2μ50
R52 47K
Q17
R51 47K
R50 3.3K
R47 100K
D5
12.0V
CN1
CN2
CN3
CN4
CN5
NF1
INPUT1
GND
INPUT2
NF2
+VCC
C2
B2
E2
+B
-B
+OUT2
-OUT2
SP OUT2
CURRENT DET
CURRENT DET
SP OUT1
-OUT1
+OUT1
-B
E1
B1
C1
GND
FIRST OFF B
N-C
M
N
O
P

(X00-252X-XX)

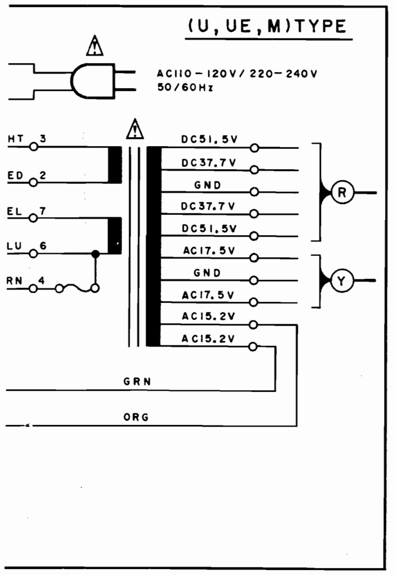
DESTINATION	Ref No.	J2	J3	W4~10	W14~16	3	E3,4	S1,2	F2
0-11	K	NO	YES	NO	NO	NO	YES	NO	NO
0-21	U, UE, M	NO	NO	YES	YES	NO	YES	YES	YES
2-71	T, E	YES	YES	NO	NO	YES	NO	NO	YES

(X85-103X-XX)

DESTINATION	Ref. No.	R1,2	R3,4	C15,16	C17,18
0-14	J, K, U, UE, M	100	J	560P	100P
2-73	T, E	1K	100	2200P	220P

(X89-101X-XX)

DESTINATION	Ref. No.	R3,4	C7,8
0-07	J, K, U, UE, M	J	120P
0-08	T, E	1K	100P


(U, UE, M)TYPE
AC110-120V/220-240V 50/60Hz
HT 3
ED 2
EL 7
LU 6
RN 4
DC51.5V
DC37.7V
GND
DC37.7V
DC51.5V
AC17.5V
GND
AC17.5V
AC15.2V
AC15.2V
GRN
ORG
R
Y

(X00-252X-XX)
IC1 : μPC7805HF

Q1,2 : 2SA999(E,F)
Q3 : 2SC2003(L,K)
Q4~8 : 2SC1923(R,O)

D1~5,13~17 : 1SS133 or HSS104
D8~12 : S5566B

IC1 : LC4966

Q1,2 : DTC124N
Q3 : DTA124N

D1~4 : HSS104 or 1SS133
D5 : HSS104A or 1SS131

(X89-101X-XX)
Q1~4,9,10,17 : 2SA992(F,E)
Q5~8,15,16 : 2SC1845(F,E)
Q11,12 : 2SC2590
Q13,14 : 2SA1110
Q18,19 : 2SC945(A)(Q,P)

D1,2,7 : 1SS176 or 1SS133
D3~6 : 1SS178 or 1SS131

SIGNAL LINE
GND LINE
+B LINE
-B LINE

(X32-1330-00)
IC1 : TC74HC00P
IC2 : YM3623B
IC3 : SM5807EP
IC4,5 : PCM56P or PCM56P-L
IC6 : μPC4574C
IC7 : μPC4570C

Q1 : DTA124EN
Q2 : DTC124EN
Q3 : 2SC1923(O,R)
Q4~7 : 2SC2878(B)
Q8,11 : 2SA733(A)(Q,P) or 2SA999(E,F)
Q9 : 2SA954(L,K)
Q10 : 2SC2003(L,K)

D1~4 : 1SS133 or HSS104
D5,6 : RD5.6ES(B2) or HZS5.6N(B2)

CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). ⚠ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returend to the customer.

- DC voltages are measured with a high impedance voltmeter. Values may vary slightly due to variations between in dividual instruments or/and units.
- Les tensions c.c. doivent être measurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.
- Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Voltmeter gemessen. Dabei schwanken die Meßwerte aufgrunt von Unterschieden zwischen einzelnen Instrumenten oder Geräten u. geringfügig.

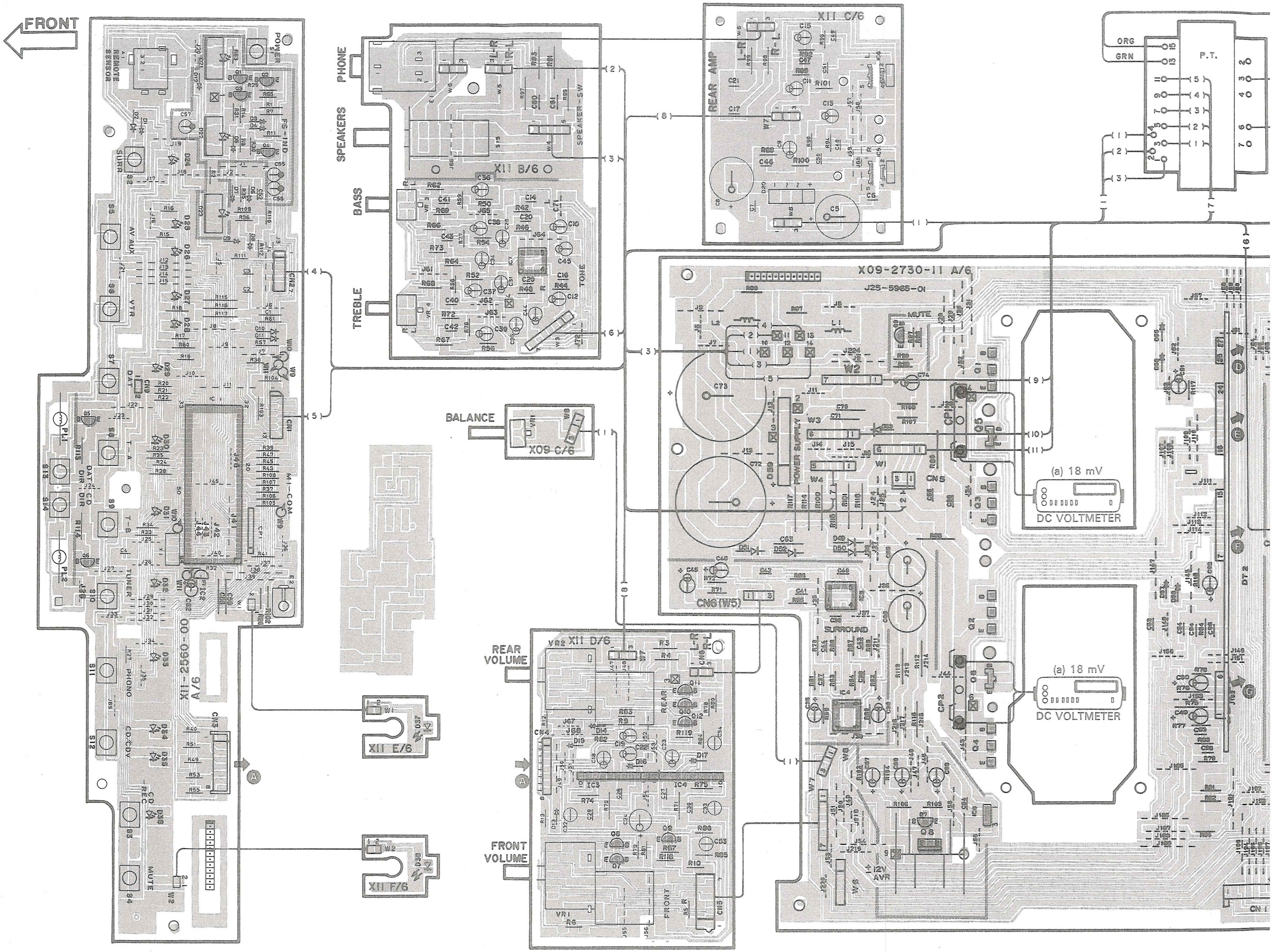
A-92(K)(2/2)

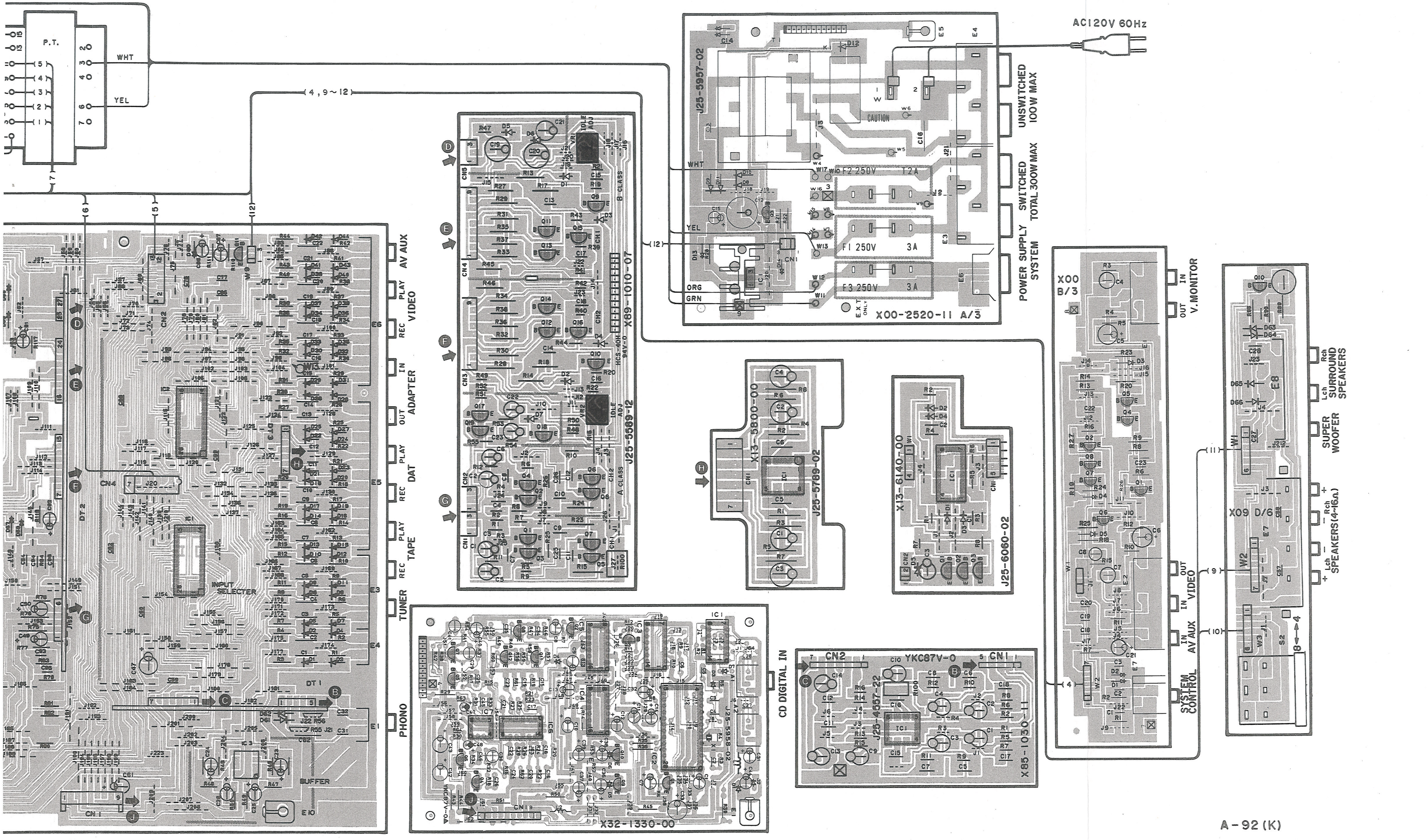
Y08-3560-11

A-92
KENWOOD

PC BOARD

FOIL SIDE VIEW

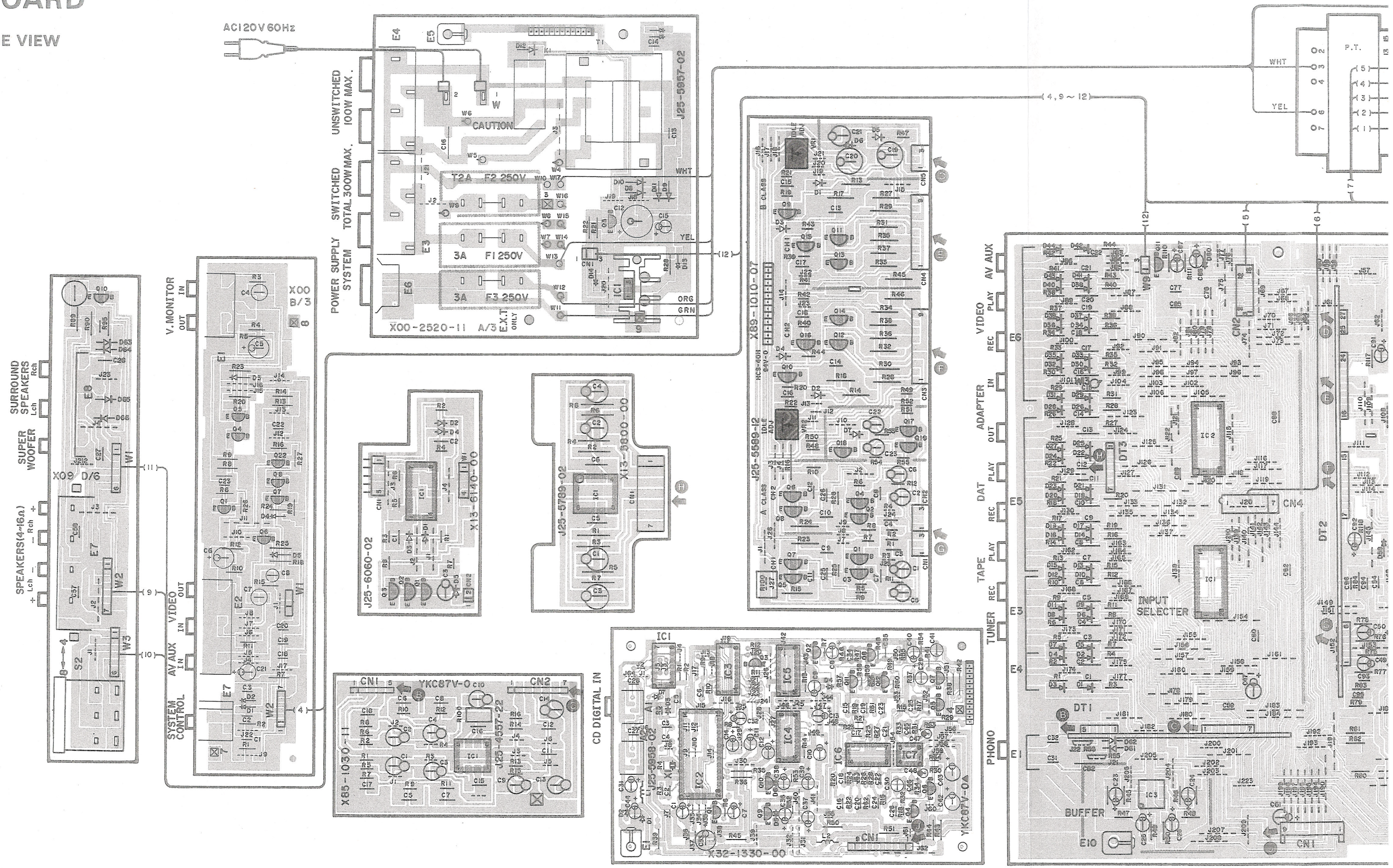

FRONT
REMOTE SENSOR
POWER
FS-IND
SURR
AV AUX
VTR
DAT
T-A
DAT CD DIR DIR
T-B
TUNER
PHONO
CD/CDV
CD REC
MUTE
M1-COM
PL1
PL2
X11-2560-00 A/6
PHONE
SPEAKERS
SPEAKER-SW
X11 B/6
BASS
TONE
TREBLE
X11 C/6
REAR AMP
P.T.
ORG
GRN
BALANCE
X09 C/6
X11 E/6
X11 F/6
REAR VOLUME
X11 D/6
FRONT VOLUME
X09-2730-11 A/6
J25-5965-01
MUTE
POWER SUPPLY
SURROUND
(a) 18 mV
DC VOLTMETER
(a) 18 mV
DC VOLTMETER
12V AVR
CN6(W5)

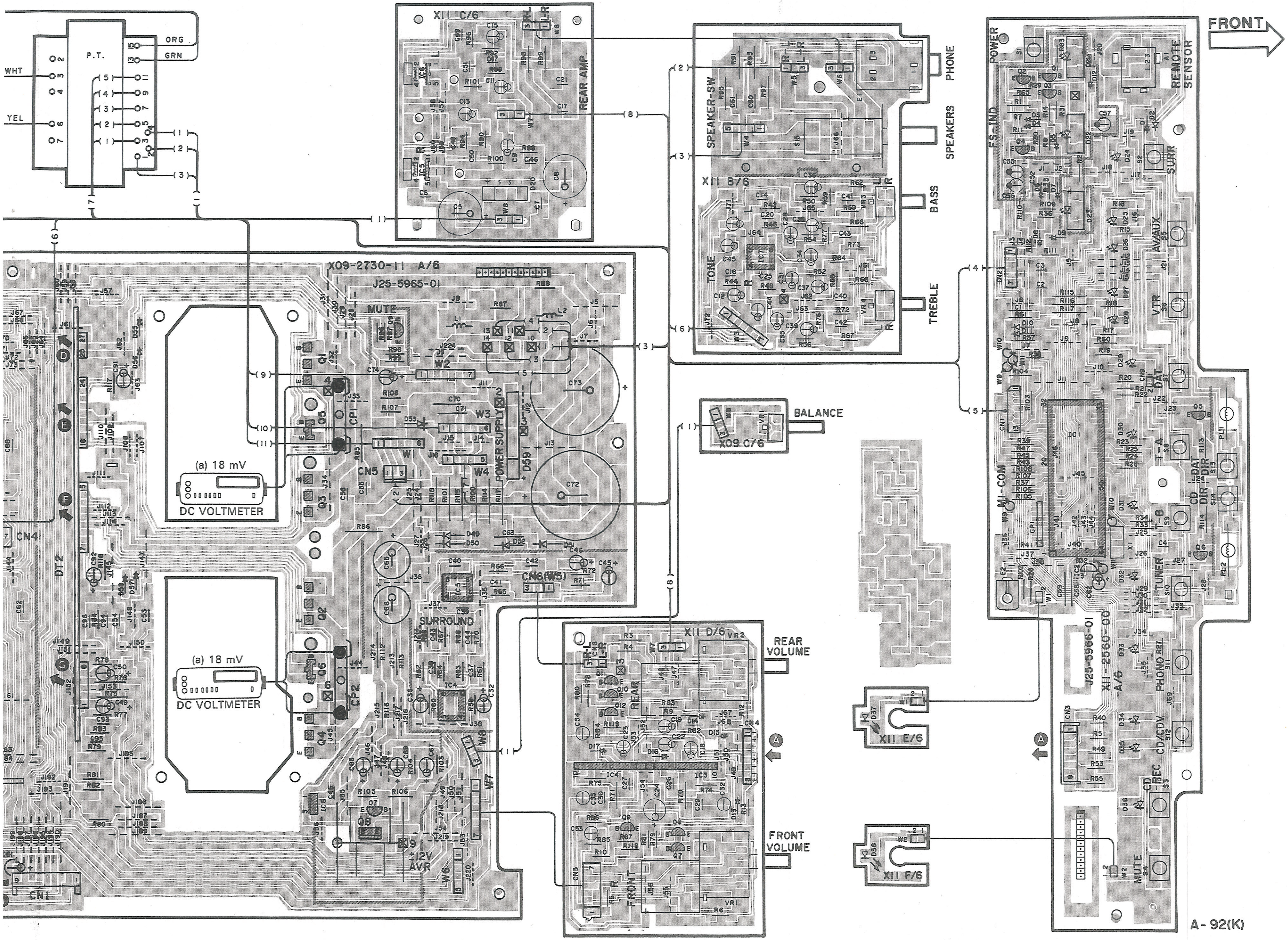

P.T.
WHT
YEL
(4, 9～12)
AC120V 60Hz
J25-5957-02
X00-2520-11 A/3
F2 250V T2A
F1 250V 3A
F3 250V 3A
CAUTION
UNSWITCHED 100W MAX
SWITCHED TOTAL 300W MAX
POWER SUPPLY SYSTEM
ORG
GRN
X89-1010-07
J25-5589-12
IDLE ADJ
B CLASS
A CLASS
AV AUX
VIDEO
PLAY
REC
IN
OUT
ADAPTER
DAT
TAPE
TUNER
PHONO
INPUT SELECTER
BUFFER
DT1
DT2
CN1
CN2
CN4
X13-5800-00
J25-5789-02
X13-6140-00
J25-6060-02
X32-1330-00
J25-5958-02
CD DIGITAL IN
YKC87V-0
J25-4557-22
X85-1030-11
X00 B/3
V.MONITOR
VIDEO
AV AUX
SYSTEM CONTROL
X09 D/6
SURROUND SPEAKERS
SUPER WOOFER
SPEAKERS(4～16Ω)
Rch
Lch
A-92(K)

Refer to the schematic diagram for the values of resistors and capacitors.

PC BOARD

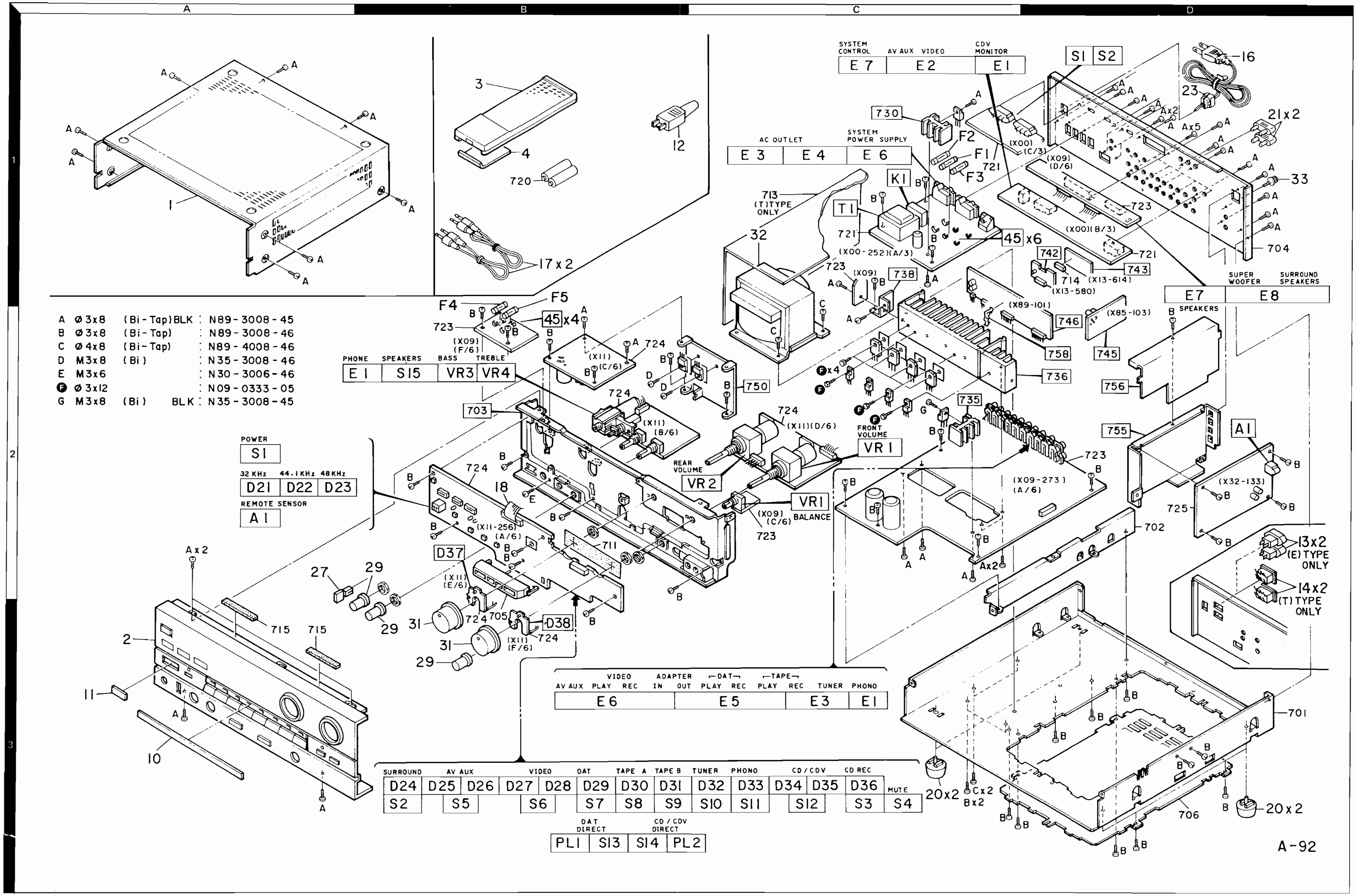
FOIL SIDE VIEW


AC120V 60Hz
UNSWITCHED 100W MAX.
SWITCHED TOTAL 300W MAX.
POWER SUPPLY SYSTEM
X00-2520-11 A/3
J25-5957-02
CAUTION
T2A F2 250V
3A F1 250V
3A F3 250V
E.X.T ONLY
WHT
YEL
ORG
GRN
P.T.
(4,9～12)
(12)
(5)
(6)
(7)
SURROUND SPEAKERS Rch Lch
SUPER WOOFER
SPEAKERS(4-16Ω) + Rch − − Lch +
X09 D/6
S2
V.MONITOR IN OUT
X00 B/3
VIDEO OUT IN
AV AUX IN
SYSTEM CONTROL
(11)
(9)
(10)
(4)
J25-6060-02
X13-6140-00
J25-5789-02
X13-5800-00
X89-1010-07
J25-5589-12
B CLASS
A CLASS
IDLE ADJ
X85-1030-11
YKC87V-0
J25-4557-22
CN1
CN2
CD DIGITAL IN
J25-5958-02
X32-1330-00
YKC87V-0A
AV AUX
VIDEO PLAY REC
ADAPTER IN OUT
DAT PLAY REC
TAPE PLAY REC
TUNER
PHONO
INPUT SELECTER
BUFFER
DT1
DT2
DT3
CN4
IC1
IC2


P.T.
ORG
GRN
WHT
YEL
X11 C/6
REAR AMP
PHONE
SPEAKER-SW
SPEAKERS
X11 B/6
BASS
TONE
TREBLE
FRONT
POWER
REMOTE SENSOR
FS-IND
SURR
AV/AUX
VTR
DAT
T-A
CD DIR
T-B
TUNER
PHONO
CD/CDV
CD REC
MUTE
MI-COM
X09-2730-11 A/6
J25-5965-01
MUTE
POWER SUPPLY
CN5
CP1
CP2
(a) 18 mV
DC VOLTMETER
(a) 18 mV
DC VOLTMETER
SURROUND
CN6(W5)
+12V AVR
X09 C/6
BALANCE
X11 D/6
REAR VOLUME
REAR
FRONT VOLUME
FRONT
X11 E/6
X11 F/6
J25-5966-01
X11-2560-00 A/6
A-92(K)
DT2
CN4
CN1

Refer to the schematic diagram for the values of resistors and capacitors.

EXPLODED VIEW


A
B
C
D
1
2
3
3
4
720
12
17x2
16
23
21x2
33
704
SYSTEM CONTROL
AV AUX
VIDEO
CDV MONITOR
E7
E2
E1
S1
S2
730
F2
F1
F3
721
(X00)(C/3)
(X09)(D/6)
AC OUTLET
SYSTEM POWER SUPPLY
E3
E4
E6
K1
T1
713 (T)TYPE ONLY
32
721
(X00-252)(A/3)
45 x6
723
(X00)(B/3)
742
714 (X13-614)
(X13-580)
743
SUPER WOOFER
SURROUND SPEAKERS
E7
E8
SPEAKERS
738
(X09)
(X89-101)
746
(X85-103)
758
745
736
756
F4
F5
45 x4
723
(X09)(F/6)
724
(X11)(C/6)
750
PHONE
SPEAKERS
BASS
TREBLE
E1
S15
VR3
VR4
703
724
(X11)(B/6)
724
(X11)(D/6)
FRONT VOLUME
VR1
735
723
755
A1
(X09-273)(A/6)
(X32-133)
725
702
13x2 (E)TYPE ONLY
14x2 (T)TYPE ONLY
POWER
S1
32KHz
44.1KHz
48KHz
D21
D22
D23
REMOTE SENSOR
A1
724
18
(X11-256)(A/6)
REAR VOLUME
VR2
VR1
(X09)(C/6)
BALANCE
723
711
Ax2
D37
27
29
29
29
31
31
(X11)(E/6)
724
705
D38
724
(X11)(F/6)
715
715
2
11
10
Ax2
701
VIDEO
ADAPTER
DAT
TAPE
AV AUX
PLAY
REC
IN
OUT
PLAY
REC
PLAY
REC
TUNER
PHONO
E6
E5
E3
E1
SURROUND
AV AUX
VIDEO
DAT
TAPE A
TAPE B
TUNER
PHONO
CD/CDV
CD REC
D24
D25
D26
D27
D28
D29
D30
D31
D32
D33
D34
D35
D36
MUTE
S2
S5
S6
S7
S8
S9
S10
S11
S12
S3
S4
DAT DIRECT
CD/CDV DIRECT
PL1
S13
S14
PL2
20x2
Cx2
Bx2
706
20x2
A-92

A	Ø3x8	(Bi-Tap)	BLK	N89-3008-45
B	Ø3x8	(Bi-Tap)		N89-3008-46
C	Ø4x8	(Bi-Tap)		N89-4008-46
D	M3x8	(Bi)		N35-3008-46
E	M3x6			N30-3006-46
Ⓕ	Ø3x12			N09-0333-05
G	M3x8	(Bi)	BLK	N35-3008-45

Parts with the exploded numbers larger than 700 are not supplied.

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	A-92						
	1	1A		A01-1681-01	METALLIC CABINET		
	2	2A	*	A20-5595-02	PANEL ASSY		
	3	1B	*	A70-0229-05	REMOTE CONTROLLER ASSY		
	4	1B		A09-0073-08	BATTERY COVER (REMOTE CONTROL)		
	10	3A		B03-2472-04	DRESSING PLATE		
	11	3A		B03-2473-04	DRESSING PLATE		
	-			B46-0092-03	WARRANTY CARD	K	
	-			B46-0094-03	WARRANTY CARD	UUE	
	-			B46-0095-03	WARRANTY CARD	UUE	
	-			B46-0122-13	WARRANTY CARD	E	
	-			B46-0143-03	WARRANTY CARD	T	
	-		*	B50-9090-00	INSTRUCTION MANUAL(ENGLISH)		
	-		*	B50-9091-00	INSTRUCTION MANUAL(FRENCH)	ME	
	-		*	B50-9092-00	INSTRUCTION MANUAL(SPANISH)	M	
	-		*	B50-9093-00	INSTRUCTION MANUAL(G,D,I)	E	
	-		*	B50-9335-00	INSTRUCTION MANUAL	E	
	-		*	B52-0273-00	CONNECTING DIAGRAM		
	-			B58-0223-04	CAUTION CARD (PRE-SET 120V)	U	
	-			B58-0513-04	CAUTION CARD (PRESET220-240)	UE	
	-			B58-0803-13	CAUTION CARD	E	
	-			B59-0092-00	SERVICE DIRECTORY	UUE	
⚠	12	1B		E03-0049-05	AC PLUG	T	
⚠	13	1D		E03-0055-05	AC OUTLET	E	
⚠	14	1D		E03-0085-05	AC OUTLET	T	
⚠	16	1D		E30-0459-05	AC POWER CORD	E	
⚠	16	1D		E30-0812-05	AC POWER CORD	UUEM	
⚠	16	1D		E30-0974-05	AC POWER CORD	K	
⚠	16	1D		E30-1416-05	AC POWER CORD	T	
	17	1B		E30-1392-05	CORD WITH PLUG		
	18	2B		E31-4530-05	WIRING HARNESS		
⚠	F1 ,2			F06-2021-05	FUSE (SEMKO) (250V T2A)	TE	
	F1 ,3		*	F05-3123-05	FUSE (250V 3.15A)	UUEM	
⚠	F1 ,3			F06-3027-05	FUSE (UL)	K	
	F2		*	F05-1629-05	FUSE (250V 1.6A)	UUEM	
⚠	F3			F05-3121-05	FUSE (SEMKO) (250V T3.15A)	TE	
⚠	F4 ,5			F06-2021-05	FUSE (SEMKO) (250V T2A)	TE	
	-		*	H01-8313-04	ITEM CARTON CASE		
	-		*	H10-3708-02	POLYSTYRENE FOAMED FIXTURE		
	-		*	H10-3709-02	POLYSTYRENE FOAMED FIXTURE		
	-			H25-0224-04	PROTECTION BAG (800X400X0.03)		
	-			H25-0232-04	PROTECTION BAG (235X350X0.03)		
	20	3D		J02-0366-15	FOOT		
	21	1D		J12-0091-05	PIN		
⚠	23	1D		J42-0083-05	POWER CORD BUSHING		
	-			J61-0307-05	WIRE BAND		
	27	3B		K27-1914-04	KNOB (BUTTON) SPEAKERS		
	29	3B		K29-2506-14	KNOB (BASS/TREBLE/BALANCE)		
	31	3B		K29-3291-04	KNOB (FRONT/REAR VOLUME)		
⚠	32	1C	*	L01-5431-05	POWER TRANSFORMER	K	
⚠	32	1C	*	L01-5432-05	POWER TRANSFORMER	T	
⚠	32	1C	*	L01-5435-05	POWER TRANSFORMER	UUEM	

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⚠	32	1C	*	L01-5438-05	POWER TRANSFORMER	E	
	33	1D		N08-0128-35	BINDING POST (GND)		
	IC1			M50461-057SP	IC(REMOTE CONTROLLER)		
	POWER SUPPLY UNIT (X00-252X-XX) 0-11 : K, 0-21 : U, UE, M, 2-71 : T, E						
	C1 ,2			CK45FB1H102K	CERAMIC 1000PF K		
	C4			C90-1334-05	NP-ELEC 47UF 10WV		
	C5 ,6			CE04KW0J471M	ELECTRO 470UF 6.3WV		
	C7 ,8			C90-1334-05	NP-ELEC 47UF 10WV		
	C12			CE04KW1E102M	ELECTRO 1000UF 25WV		
	C13 ,14			CK45FF1H103Z	CERAMIC 0.010UF Z		
	C15			CE04KW1V100M	ELECTRO 10UF 35WV		
⚠	C16			C91-0647-05	CERAMIC 0.01UF P	TE	
⚠	C16			C91-0971-05	FILM 0.01UF 250WV	KUUEM	
	C21			CE04KW1E101M	ELECTRO 100UF 25WV		
	C22 ,23			CC45FSL1H820J	CERAMIC 82PF J		
	C24			CE04KW1H010M	ELECTRO 1.0UF 50WV		
	E1	1C		E13-0297-05	PHONO JACK CDV MONITOR		
	E2	1C		E13-0309-05	PHONO JACK AUAVX VIDEO		
⚠	E3 ,4	1C		E03-0095-05	AC OUTLET (4P)	KUUEM	
	E6	1C		E08-0411-05	RECTANGULAR RECEPTACLE(POW SUP		
	E7	1C		E11-0188-05	MINIATURE PHONE JACK(CONTROL)		
	45	1C		J13-0054-05	FUSE CLIP		
⚠	T1	1C	*	L01-5471-05	POWER TRANSFORMER	K	
⚠	T1	1C	*	L01-5472-05	POWER TRANSFORMER	TE	
⚠	T1	1C	*	L01-5473-05	POWER TRANSFORMER	UUEM	
	K1	1C		S51-1052-05	MAGNETIC RELAY		
⚠	S1 ,2			S31-2131-05	SLIDE SWITCH (POWER TYPE)	UUEM	
	D1 -5			HSS104	DIODE		
	D1 -5			1SS133	DIODE		
	D8 -12			S5566B	DIODE		
	D13 -15			HSS104	DIODE		
	D13 -17			1SS133	DIODE		
	IC1			UPC7805HF	IC(VOLTAGE REGULATOR/ +5V)		
	Q1 ,2			2SA999(E,F)	TRANSISTOR		
	Q3			2SC2003(L,K)	TRANSISTOR		
	Q4 -8			2SC1923(R,O)	TRANSISTOR		
	AUDIO UNIT (X09-273X-XX) 0-11 : K, 0-21 : U, UE, M, 2-71 : T, E						
	C3 -8			C91-0749-05	CERAMIC 220PF K	TE	
	C9 ,10			CC45FSL1H221J	CERAMIC 220PF J		
	C11 ,12			CC45FSL1H221J	CERAMIC 220PF J	TE	
	C13 -22			C91-0749-05	CERAMIC 220PF K	TE	
	C15 ,16			C91-0757-05	CERAMIC 0.001UF K	KUUEM	
	C27 ,28			CC45FSL1H221J	CERAMIC 220PF J	TE	
	C31 ,32			CC45FSL1H121J	CERAMIC 120PF J	TE	
	C35 ,36			CE04KW1H010M	ELECTRO 1.0UF 50WV		
	C37 ,38			CF92FV1H153J	MF 0.015UF J		
	C39 -42			CK45FB1H391K	CERAMIC 390PF K		
	C43 ,44			CF92FV1H222J	MF 2200PF J		
	C45 ,46			CE04KW1H010M	ELECTRO 1.0UF 50WV		
	C47 ,48			CE04KW1V100M	ELECTRO 10UF 35WV		
	C49 ,50			CE04KW1C220M	ELECTRO 22UF 16WV		

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Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Desti-nation 仕向	Re-marks 備考
C53 ,54			CF92FV1H334J	MF 0.33UF J		
C55 ,56			CF92FV1H473J	MF 0.047UF J		
C57 ,58			CK45FF1H103Z	CERAMIC 0.010UF Z	TE	
C59 ,60			CF92FV1H564J	MF 0.56UF J		
C62			CK45FF1H472Z	CERAMIC 4700PF Z		
C63			CK45FE2H103P	CERAMIC 0.010UF P		
C64			CK45FF1H103Z	CERAMIC 0.010UF Z		
C65 ,66			CE04KW1V102M	ELECTRO 1000UF 35WV		
C67 -69			CE04KW1C470M	ELECTRO 47UF 16WV		
C70 ,71			CK45FE2H103P	CERAMIC 0.010UF P		
⚠ C72 ,73			C90-1589-05	ELECTRO 6800UF 50WV	K	
⚠ C72 ,73			C90-1590-05	ELECTRO 6800UF 63WV	UUEMTE	
C74			CE04KW1J100M	ELECTRO 10UF 63WV		
C76 ,77			CK45FF1H472Z	CERAMIC 4700PF Z		
C82			C91-0085-05	CERAMIC 0.022UF N		
C85			CK45FF1H472Z	CERAMIC 4700PF Z		
C86			CE04KW1C470M	ELECTRO 47UF 16WV		
C87			CE04KW1A101M	ELECTRO 100UF 10WV		
C88 ,89			CF92FV1H564J	MF 0.56UF J		
C91 ,92			CE04KW1C470M	ELECTRO 47UF 16WV		
C93 ,94			CC45FSL1H150J	CERAMIC 15PF J	TE	
E1	3C		E13-0235-05	PHONO JACK (2P)PHONO		
E3	3C		E13-0497-05	PHONO JACK (4P)REC/TUNER		
E5 ,6	3B,3C		E13-0814-05	PHONO JACK (8P)AV AUX/PLAY/REC		
E7	1D		E20-0459-05	LOCK TERMINAL BOARD		
E8	1D		E13-0314-05	PHONO JACK		
L1 ,2			L39-0085-05	PHASE-COMPENSATION COIL		
F	2C		N09-0333-05	TAPPING SCREW (3X12)		
CP1 ,2			R90-0187-05	MULTI-COMP 0.22X2 K 5W		
R81 ,82			RD14AB2E330J	FL-PROOF RD 33 J 1/4W		
R85 ,86			RS14DB3D100J	FL-PROOF RS 10 J 2W		
R87 ,88			RD14AB2E100J	FL-PROOF RD 10 J 1/4W		
R100,101			RS14DB3D220J	FL-PROOF RS 22 J 2W	K	
R100,101			RS14DB3D330J	FL-PROOF RS 33 J 2W	UUEMTE	
R107			RD14AB2E100J	FL-PROOF RD 10 J 1/4W		
R108			RD14AB2E332J	FL-PROOF RD 3.3K J 1/4W		
R112			RS14DB3D100J	FL-PROOF RS 10 J 2W	UUEMTE	
R113		*	RS14DB3D820J	FL-PROOF RS 82 J 2W	UUEMTE	
R114,115		*	RS14DB3D150J	FL-PROOF RS 15 J 2W	UUEMTE	
R114,115			RS14DB3D220J	FL-PROOF RS 22 J 2W	K	
R116			RS14DB3D100J	FL-PROOF RS 10 J 2W	UUEMTE	
VR1			R05-5025-05	POTENTIOMETER		
S2			S31-2136-05	SLIDE SWITCH (POWER TYPE)	UUEMTE	
D5 -44		*	HSS104	DIODE		
D5 -44			1SS133	DIODE		
D49 -53			S5566B	DIODE		
D55 -58		*	HSS104	DIODE		
D55 -58			1SS133	DIODE		
⚠ D59			RBV-602LFA	DIODE		
D60 -66		*	HSS104	DIODE		
D60 -66			1SS133	DIODE		
IC1			TC9163N	IC(BILATERAL SWITCH X16)		

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IC2			TC9164N	IC(16CH BILATERAL SELECTOR SW)		
IC4 ,5			UPC4570C	IC(OP AMP X2)		
IC6			UPC7812HF	IC(VOLTAGE REGULATOR/ +12V)		
⚠ Q1 ,2			2SC3855*5	TRANSISTOR		
⚠ Q3 ,4			2SA1491*5	TRANSISTOR		
Q5 ,6			2SC4137(U,V)	TRANSISTOR		
Q7			2SA733(A)(Q,P)	TRANSISTOR		
Q7			2SA933S(Q,R)	TRANSISTOR		
Q8			2SB941	TRANSISTOR		
Q9			2SA733(A)(Q,P)	TRANSISTOR		
Q9			2SA933S(Q,R)	TRANSISTOR		
Q10			2SC2878(B)	TRANSISTOR		
Q11			2SA999(E,F)	TRANSISTOR		
CONTROL UNIT (X11-2560-00)						
D21 -23			B30-0499-05	LED(LN0202RP2)		
D24 -36			B30-0431-05	LED(LN21CPH)		
D37 ,38			B30-1012-05	LED(SLP-981C-50)		
PL1 ,2		*	B30-1260-05	LAMP		
C1			C91-0769-05	CERAMIC 0.01UF M		
C2 ,3			CK45FF1H103Z	CERAMIC 0.010UF Z		
C4			CF92FV1H104J	MF 0.10UF J		
C5			CE04KW1E222M	ELECTRO 2200UF 25WV		
C6 ,7			CK45FF1H103Z	CERAMIC 0.010UF Z		
C8			CE04KW1E222M	ELECTRO 2200UF 25WV		
C9 -12			CE04KW1H010M	ELECTRO 1.0UF 50WV		
C13			CE04KW1V100M	ELECTRO 10UF 35WV		
C14			CC45FSL1H221J	CERAMIC 220PF J		
C15			CE04KW1V100M	ELECTRO 10UF 35WV		
C16			CC45FSL1H221J	CERAMIC 220PF J		
C17			CF92FV1H224J	MF 0.22UF J		
C18 ,19			CE04KW1V100M	ELECTRO 10UF 35WV		
C20			CK45FF1H472Z	CERAMIC 4700PF Z		
C21			CF92FV1H224J	MF 0.22UF J		
C22 ,23			CE04KW1V100M	ELECTRO 10UF 35WV		
C24			CE04KW0J471M	ELECTRO 470UF 6.3WV		
C25			CK45FF1H472Z	CERAMIC 4700PF Z		
C26 ,27			CF92FV1H104J	MF 0.10UF J		
C28			CE04KW1H4R7M	ELECTRO 4.7UF 50WV		
C29 ,30			CF92FV1H104J	MF 0.10UF J		
C31			CE04KW1H4R7M	ELECTRO 4.7UF 50WV		
C32 ,33			C90-1333-05	NP-ELEC 22UF 10WV		
C34 ,35			CE04KW1V100M	ELECTRO 10UF 35WV		
C36 -39			CE04KW1H010M	ELECTRO 1.0UF 50WV		
C40 -43			CF92FV1H473J	MF 0.047UF J		
C44 ,45			CE04KW1C470M	ELECTRO 47UF 16WV		
C46 -49			CC45FSL1H331J	CERAMIC 330PF J		
C50 ,51			CK45FF1H103Z	CERAMIC 0.010UF Z		
C52			CK45FB1H102K	CERAMIC 1000PF K		
C53 ,54			C90-1351-05	NP-ELEC 3.3UF 50WV		
C55 -57			CE04JW1A101M	ELECTRO 100UF 10WV		
C58 ,59			C91-0769-05	CERAMIC 0.01UF M		
C60 ,61			CF92FV1H224J	MF 0.22UF J		
C62			CC45FSL1H221J	CERAMIC 220PF J		
CN1			E10-1306-05	FLAT CABLE CONNECTOR		

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E1	2B		E11-0162-05	PHONE JACK (3P)		
X1			L78-0209-05	RESONATOR (4.194MHZ)		
X1			L78-0218-05	RESONATOR		
CP1			R90-0805-05	MULTI-COMP 10KX8 J 1/4W		
R74 ,75			RD14AB2E100J	FL-PROOF RD 10 J 1/4W		
R91			RS14DB3D331J	FL-PROOF RS 330 J 2W		
R93			RS14DB3D331J	FL-PROOF RS 330 J 2W		
R95			RS14DB3D331J	FL-PROOF RS 330 J 2W		
R97			RS14DB3D331J	FL-PROOF RS 330 J 2W		
R98 ,99			RS14DB3A2R7J	FL-PROOF RS 2.7 J 1W		
R113,114			RS14DB3A471J	FL-PROOF RS 470 J 1W		
VR1 ,2			R29-5021-05	POTENTIOMETER(100KX2)		
VR3 ,4			R10-3038-05	POTENTIOMETER		
S1 -14			S40-1064-05	PUSH SWITCH		
S15			S40-4056-05	PUSH SWITCH		
D1 -15			HSS104	DIODE		
D1 -15			1SS133	DIODE		
D16 ,17			HZS5.1N(B2)	ZENER DIODE		
D16 ,17			RD5.1ES(B2)	ZENER DIODE		
D20			KBP02ML-6127	DIODE		
IC1		*	CXP5016-315S	IC(MICROPROCESSOR)		
IC2			M51951ASL	IC(SYSTEM RESET)		
IC3 ,4			BA6109	IC(MOTOR DRIVER)		
IC3 ,4			LB1641	IC(MOTOR DRIVER)		
IC5 ,6			UPC1238V*1	IC(POWER AMP)		
IC7			UPC4570C	IC(OP AMP X2)		
Q1 -4			2SC1740S(Q,R)	TRANSISTOR		
Q1 -4			2SC945(A)(Q,P)	TRANSISTOR		
Q5 ,6			2SC2003(L,K)	TRANSISTOR		
Q5 ,6			2SC2320(E,F)	TRANSISTOR		
Q7 ,8			2SD1302(S,T)	TRANSISTOR		
Q9			2SA733(A)(Q,P)	TRANSISTOR		
Q9			2SA933S(Q,R)	TRANSISTOR		
Q10 ,11			2SD1302(S,T)	TRANSISTOR		
Q12			2SA733(A)(Q,P)	TRANSISTOR		
Q12			2SA933S(Q,R)	TRANSISTOR		
A1	2A		W02-0910-05	ELECTRIC CIRCUIT MODULE		
BAFA UNIT (X13-5800-00)						
C1 ,2			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C3 ,4			CE04KW1V220M	ELECTRO 22UF 35WV		
IC1			UPC4570C	IC(OP AMP X2)		
SUB-CIRCUIT UNIT (X13-6140-00)						
C1 ,2			CC45FSL1H221J	CERAMIC 220PF J		
C3			CE04KW1H010M	ELECTRO 1.0UF 50WV		
D1 -4			HSS104	DIODE		
D1 -4			1SS133	DIODE		
D5			HSS104A	DIODE		
D5			1SS131	DIODE		
IC1			LC4966	IC(CMOS LOGIC BILATERAL SW)		
Q1 ,2			DTC124EN	DIGITAL TRANSISTOR		

E: Scandinavia & Europe **K:** USA **P:** Canada
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⚠ indicates safety critical components.

PARTS LIST

⁎ New Parts

Parts without **Parts No.** are not supplied.
Les articles non mentionnes dans le **Parts No.** ne sont pas fournis.
Teile ohne **Parts No.** werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Desti-nation 仕向	Re-marks 備考
Q10			2SC2003(L,K)	TRANSISTOR		
Q11			2SA733(A)(Q,P)	TRANSISTOR		
Q11			2SA933S(Q,R)	TRANSISTOR		
Q12			2SC1740S(Q,R)	TRANSISTOR		
Q12			2SC945(A)(Q,P)	TRANSISTOR		
A1			W02-0937-05	OPTIC RECEIVING MODULE		
PREAMPLIFIER UNIT (X85-103X-XX) 0-11 : K, U, UE, M, 2-72 : T, E						
C1 ,2			CE04FW1C100M	ELECTRO 10UF 16WV		
C3 ,4			CE04FW1A470M	ELECTRO 47UF 10WV		
C5			CF92FV1H123J	MF 0.012UF J		
C6			CF92FV1H123J	MF 0.012UF J		
C7			CF92FV1H332J	MF 3300PF J		
C8			CF92FV1H332J	MF 3300PF J		
C9 ,10			CE04KW1V100M	ELECTRO 10UF 35WV		
C11 ,12			CK45FF1H103Z	CERAMIC 0.010UF Z		
C15 ,16			CK45FB1H222K	CERAMIC 2200PF K	TE	
C15 ,16			CK45FB1H561K	CERAMIC 560PF K	KUUEM	
C17 ,18			CC45FSL1H101J	CERAMIC 100PF J	KUUEM	
C17 ,18			CC45FSL1H221J	CERAMIC 220PF J	TE	
IC1			AN6556	IC(OP AMP X2)		
IC1			M5218P	IC(OP AMP X2)		
MAIN AMPLIFIER UNIT (X89-101X-XX) 0-07 : K, U, UE, M, 0-08 : T, E						
C1 ,2			CE04KW1V100M	ELECTRO 10UF 35WV		
C3 ,4			CC45FSL1H221J	CERAMIC 220PF J		
C7 ,8			CC45FSL1H101J	CERAMIC 100PF J	TE	
C9 ,10			CC45FSL1H680J	CERAMIC 68PF J		
C11 -14			CC45FSL1H221J	CERAMIC 220PF J		
C15 ,16			CK45FF1H103Z	CERAMIC 0.010UF Z		
C17 ,18			CK45FB1H222K	CERAMIC 2200PF K		
C19 ,20			CE04KW1J221M	ELECTRO 220UF 63WV		
C21			CE04KW1E101M	ELECTRO 100UF 25WV		
C22			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C23			CE04KW1C330M	ELECTRO 33UF 16WV		
C25 ,26			CC45FSL1H020C	CERAMIC 2.0PF C		
R13 ,14			RD14AB2E221J	FL-PROOF RD 220 J 1/4W		
R17 ,18			RD14AB2E221J	FL-PROOF RD 220 J 1/4W		
R23 ,24			RD14AB2E271J	FL-PROOF RD 270 J 1/4W		
R27 -30			RD14AB2E4R7J	FL-PROOF RD 4.7 J 1/4W		
R31 -34			RD14AB2E221J	FL-PROOF RD 220 J 1/4W		
R35 -38			RD14AB2E2R2J	FL-PROOF RD 2.2 J 1/4W		
R45 ,46			RD14AB2E101J	FL-PROOF RD 100 J 1/4W		
VR1 ,2			R12-1070-05	TRIMMING POT.(1K)		
D1 ,2			1SS133	DIODE		
D1 ,2			1SS176	DIODE		
D3 -6			1SS131	DIODE		
D3 -6			1SS178	DIODE		
D7			1SS133	DIODE		
D7			1SS176	DIODE		
Q1 -4			2SA992(F,E)	TRANSISTOR		
Q5 -8			2SC1845(F,E)	TRANSISTOR		
Q9 ,10			2SA992(F,E)	TRANSISTOR		
Q11 ,12			2SC2590	TRANSISTOR		

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Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Desti-nation 仕向	Re-marks 備考
Q13 ,14			2SA1110	TRANSISTOR		
Q15 ,16			2SC1845(F,E)	TRANSISTOR		
Q17			2SA992(F,E)	TRANSISTOR		
Q18 ,19			2SC945(A)(Q,P)	TRANSISTOR		

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SPECIFICATIONS

Amplifier (A-92)

Power Output

50 watts per channel minimum RMS, both channels driven, at 8 ohms from 20 Hz to 20,000 Hz with no more than 0.09% total harmonic distortion.

Maximum continuous output power (DIN) 1 kHz at 8 Ω	
FRONT	75 W + 75 W
REAR	8 W + 8 W
Maximum continuous output power (IEC) from 63 Hz to 12,500 Hz, 0.7% THD at 8 ohms	70 W + 70 W
Total Harmonic Distortion (20 Hz to 20,000 Hz 8 ohms)/	
At rated output	0.09%
At 1/2 rated output	0.04%
Intermodulation distortion (60 Hz:7 kHz = 4:1)	0.02% at rated output into 8 ohms
Signal-to-Noise ratio (IHF-A)	
PHONO MM (2.5 mV)	75 dB
TUNER, AUX, TAPE PLAY	95 dB
Signal-to-Noise ratio at unweighted, 80 mW output (DIN)	
PHONO (MM)	55 dB
TUNER, AUX, TAPE PLAY	55 dB
Tone Control	
Bass	±10 dB at 100 Hz
Treble	±10 dB at 10 kHz
Damping Factor	60 at 50 Hz
Channel Separation (DIN) at 1,000 Hz	
PHONO (Terminated with 2.2 kΩ)	60 dB
AUX (Terminated with 47 kΩ +250 pF)	50 dB
Input sensitivity/impedance	
PHONO MM	2.5 mV/47 kohms
TUNER, AUX., TAPE PLAY	150 mV/47 kohms

D/A Converter section

Input sampling frequencies	32 kHz/44.1 kHz/48 kHz
Signal to noise ratio	95 dB
Total harmonic distortion	0.03% at 1 kHz
Channel separation	94 dB at 1 kHz

General

Power consumption	2.5 A (U.S.A. models) 200 W (Other country models)
Dimensions	W: 360 mm (14-3/16") H: 129 mm (5-1/16") D: 368 mm (14-1/2")
Weight (Net)	8.5 kg (18.7 lb)

Remote control unit (RC-92)

Maximum remote-controllable distance	6 m (on an axis of optical sensor)
Remote control system	Infrared control system
Battery for remote control unit	Size "AA" (R6) ×2

A-92

Note
Component and circuitry are subject to modification to insure best operation under differing local conditions. This manual is based on the U.S.A. (K) standard, and provides information on regional circuit modification through use of alternate schematic diagrams, and information on regional component variations through use of parts list.

KENWOOD CORPORATION
Shionogi Shibuya Building, 17-5, 2-chome Shibuya, Shibuya-ku, Tokyo 150, Japan

KENWOOD U.S.A. CORPORATION
2201 East Dominguez Street, Long Beach, CA 90810;
550 Clark Drive, Mount Olive, NJ 07828, U.S.A.

KENWOOD ELECTRONICS CANADA INC.
P.O. Box 1075 959 Gana Court, Mississauga, Ontario, Canada L4T 4C2

KENWOOD ELECTRONICS BENELUX N.V.
Mechelsesteenweg 418 B-1930 Zaventem, Belgium

KENWOOD ELECTRONICS DEUTSCHLAND GMBH
Rembrücker-Str. 15, 6056 Heusenstamm, West Germany

TRIO-KENWOOD FRANCE S.A.
Hi-Fi•VIDEO•CAR Hi-Fi
13, Boulevard Ney, 75018 Paris, France

TRIO-KENWOOD U.K. LTD.
17 Bristol Road, The Metropolitan Centre, Greenford, Middx. UB6 8UP England

KENWOOD ELECTRONICS AUSTRALIA PTY. LTD.
4E Woodcock Place, Lane Cove, N.S.W. 2066, Australia

KENWOOD & LEE ELECTRONICS, LTD.
Wang Kee Building, 4 th Floor, 34-37, Connaught Road, Central, Hong Kong